

Low Noise, High IP3

Monolithic Amplifier

PMA-5453+

50Ω 0.05 to 6 GHz



3mm x 3mm MCLP Pkg

The Big Deal

- Ultra Low Noise Figure, 0.6 dB
- High IP3/Low Current, 60mA
- Wideband, up to 6 GHz

Product Overview

Mini-Circuits PMA-5453+ is a E-PHEMT based Ultra-Low Noise MMIC Amplifier operating from 50 MHz to 6 GHz with a unique combination of low noise and high IP3 making this amplifier ideal for sensitive receiver applications. This design operates on a single 3V supply at only 60 mA and is internally matched to 50 Ohms.

Key Features

Feature	Advantages
Ultra Low Noise, 0.6 dB	Outstanding Noise Figure, measured in a 50 Ohm environment without any external matching
High IP3, 35 dBm	Combining Low Noise and High IP3 makes this MMIC amplifier ideal for Low Noise Receiver Front End (RFE) because it gives the user advantages at both ends of the dynamic range: sensitivity & two-tone spur-free dynamic range
Low Current, 60 mA	At only 60mA, the PMA-5453+ is ideal for remote applications with limited available power or densely packed applications where thermal management is critical.
Broad Band	Operating over a broadband the PMA-5453+ covers the primary wireless communications bands: Cellular, PCS, LTE, WiMAX
Internally Matched	No external matching elements required to achieve the advertised noise and output power over the full band
MCLP Package	Low Inductance, repeatable transitions, excellent thermal pad
Max Input Power, +20 dBm	Ruggedized design operates up to input powers of +20dBm without the need of an external limiter
High Reliability	Low, small signal operating current of 60 mA nominal maintains junction temperatures typically below 110°C at 85°C ground lead temperature

Notes

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Low Noise, High IP3

Monolithic Amplifier

0.05-6 GHz

Product Features

- Single Positive Supply Voltage, 3V, $I_d=60\text{mA}$
- Ultra Low Noise Figure, 0.6 dB typ. at 0.5 GHz
- High IP3, 35 dBm typ. 1GHz
- Gain, 19 dB typ. at 1 GHz
- Output Power, up to +19.5dBm typ.
- Micro-miniature size - 3mm x 3mm
- Aqueous washable

Typical Applications

- Cellular
- ISM
- GSM
- WCDMA
- LTE
- WiMAX
- WLAN
- UNII and HIPERLAN



Generic photo used for illustration purposes only

PMA-5453+

CASE STYLE: DQ849

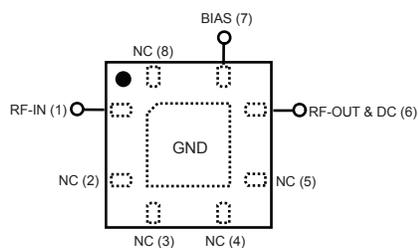
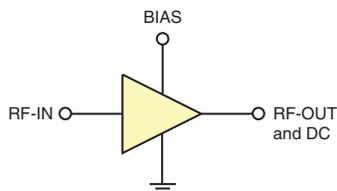
+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

PMA-5453+ is a high dynamic range, low noise, high IP3, high output power, monolithic amplifier. Manufactured using E-PHEMT* technology enables it to work with a single positive supply voltage.

simplified schematic and pad description



Function	Pad Number	Description (See Application Circuit, Fig. 3)
RF-IN	1	RF input pad
RF-OUT & DC	6	RF output pad (connected to RF-OUT via blocking external cap C2, and Supply voltage V_s via RF Choke L1)
BIAS	7	Bias pad (connected to V_s via Rbias)
GND	paddle in center of bottom	Connected to ground
NOT USED	2,3,4,5,8	No internal connection; recommended use: per PCB Layout PL-299

*Enhancement mode Pseudomorphic High Electron Mobility Transistor.

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PMA-5453+
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Electrical Specifications⁽¹⁾ at 25°C, Zo=50Ω, (refer to characterization circuit, Fig. 1)

Parameter	Condition (GHz)	Min.	Typ.	Max.	Units
Frequency Range		0.05		6.0	GHz
DC Voltage (V _d)			3.0		V
DC Current (I _d) ⁽⁶⁾		40	60	71	mA
DC Current (I _{Rbias})			4.0		mA
Noise Figure	0.05		1.2	—	dB
	0.5		0.6	—	
	1.0		0.7	—	
	2.0		0.9	1.3	
	3.0		1.3	—	
	4.0		1.5	—	
	5.0		1.8	—	
Gain	0.05	—	25.8	—	dB
	0.5	—	23.0	—	
	1.0	—	19.3	—	
	2.0	12.8	14.3	15.6	
	3.0	—	11.1	—	
	4.0	—	9.0	—	
	5.0	—	7.2	—	
Input Return Loss	0.05-0.5		10.0		dB
	0.5-6		7.0		
Output Return Loss	0.05-0.1		16.0		dB
	0.1-6		18.0		
Output IP3	0.05		32.1		dBm
	0.5		33.1		
	1.0		35.0		
	2.0		36.8		
	3.0		37.4		
	4.0		38.8		
	5.0		39.0		
Output Power @ 1 dB compression ⁽²⁾	0.05		19.4		dBm
	0.5		19.2		
	1.0		19.1		
	2.0		19.4		
	3.0		19.6		
	4.0		19.8		
	5.0		19.9		
6.0		20.0			
DC Current Variation vs. Temperature ⁽³⁾			-0.033		mA/°C
Thermal Resistance			128		°C/W

Absolute Maximum Ratings⁽⁴⁾

Parameter	Ratings
Operating Temperature ⁽⁵⁾	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Channel Temperature	150°C
DC Voltage (Pad 6)	5V
Power Dissipation	500mW
DC Current (Pad 6)	130mA
Bias Current (Pad 7)	10mA
Input Power ⁽⁷⁾	20dBm

- ⁽¹⁾ Measured on Mini-Circuits Characterization test board TB-502+ See Characterization Test Circuit (Fig. 1)
- ⁽²⁾ P1dB specified with external current limiting of 70mA; Capable of higher P1dB at higher current (see Fig. 2)
- ⁽³⁾ (Current at 85°C - Current at -45°C)/130
- ⁽⁴⁾ Permanent damage may occur if any of these limits are exceeded. These maximum ratings are not intended for continuous normal operation.
- ⁽⁵⁾ Defined with reference to ground pad temperature.
- ⁽⁶⁾ Specified DC current consumption is under small signal conditions. Current will increase with input RF Power. To maintain maximum current consumption, external DC current limiting circuits are required on Vd line.
- ⁽⁷⁾ Maximum input power is specified based upon external Vd current limiting of 80 mA. Maximum input power will degrade without external current limiting.

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Characterization Test Circuit

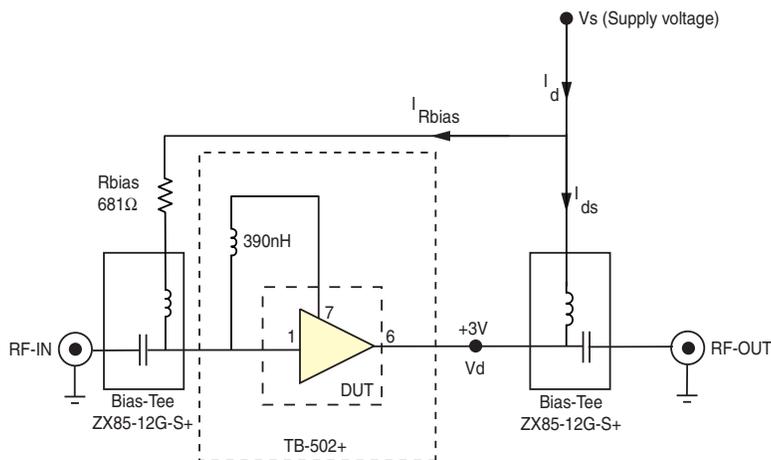


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-502+) Gain, Output power at 1dB compression (P1dB), Output IP3 (OIP3) and Noise Figure measured using Agilent's N5242A PNA-X Microwave network analyzer.

Conditions:

1. Gain: Pin=-25 dBm
2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 5 dBm/tone at output.
3. Vs adjusted for 3V at device (Vd), compensating loss of bias tee.

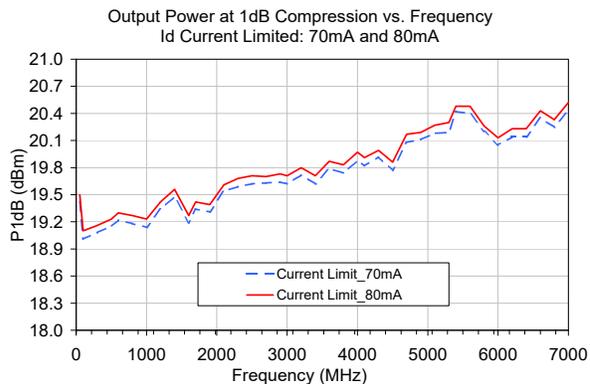
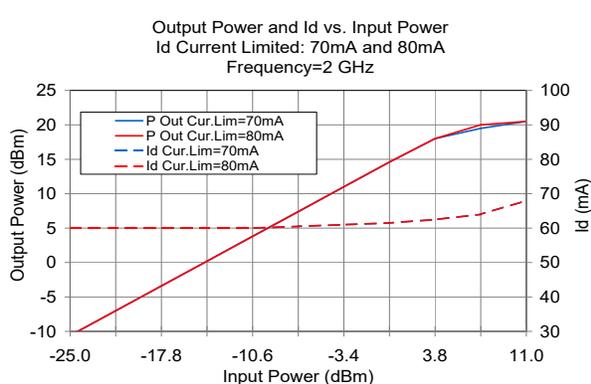


Fig 2. Output Power and Id vs. Input Power and Frequency. Performance measured on Mini-Circuits Characterization test board TB-502+. See Characterization Test Circuit (Fig. 1) Measurements performed with current (Id) limited as noted.

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Recommended Application Circuit

(refer to evaluation board for PCB Layout and component values)

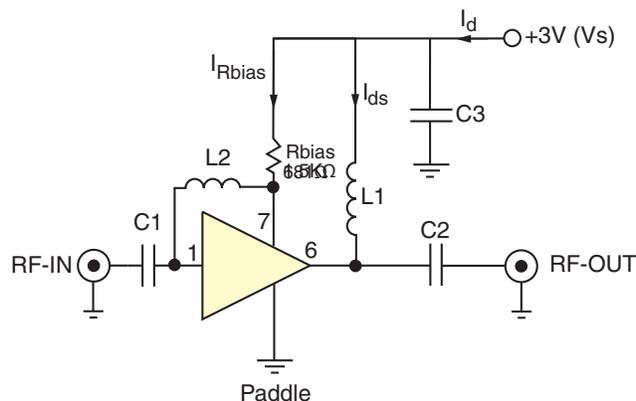


Fig 3. Recommended Application Circuit
 Note: Resistance of L1, 0.1-0.2Ω typically

Typical Current (I_d) as a function of R_{bias}
 ($V_d = 3V$)

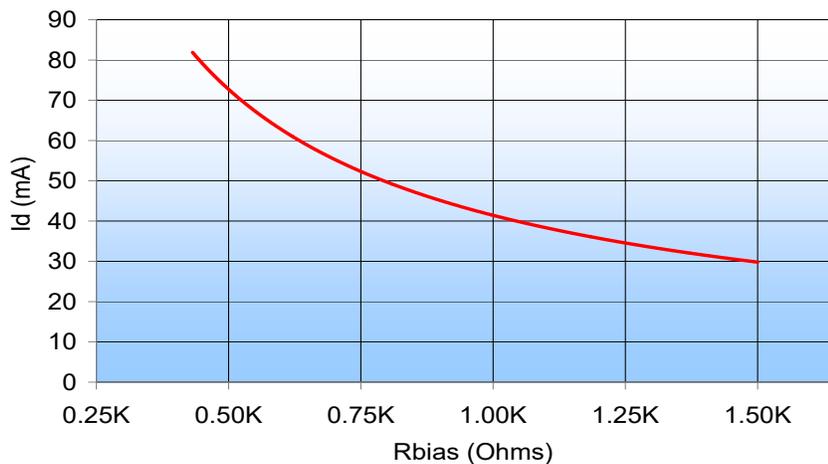


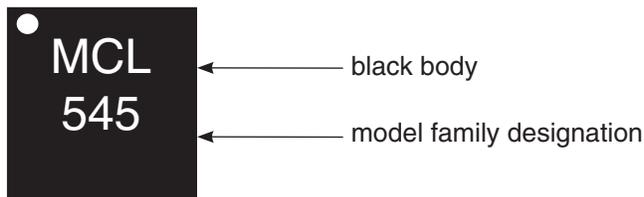
Fig 4. I_d varies as a function of R_{bias} . The I_d current range is defined based upon the specific R_{bias} value noted in the Application Circuit (Fig 3). R_{bias} may be adjusted to optimize I_d for a customer's application. RF performance will vary accordingly.

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Product Marking



Marking may contain other features or characters for internal lot control

Additional Detailed Technical Information

Additional information is available on our web site www.minicircuits.com. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: DQ849

Plastic package, exposed paddle, lead finish: tin-silver over nickel

Tape & Reel: F104

Standard quantities available on reel: 7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices.

Suggested Layout for PCB Design: PL-299

Evaluation Board: TB-501-3+ (50-5000 MHz)

Environmental Ratings: ENV08T1

ESD Rating

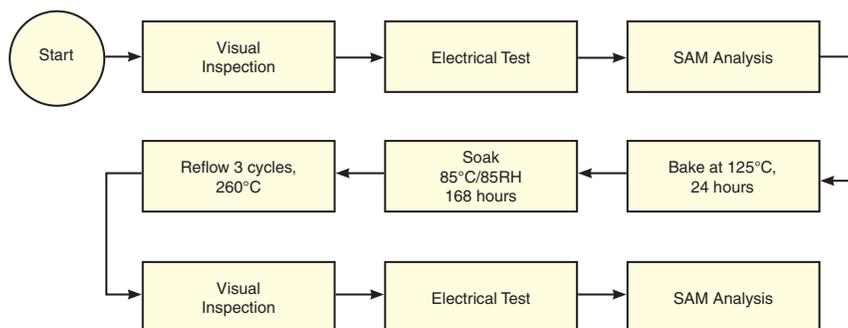
Human Body Model (HBM): Class 1A (250V to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 40V

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart



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