

Engineering/Process Change Notice

ECN/PCN No.: 4111

	For Man	ufacturer			
Product Description: PLASTIC SMD MEMS OSCILLATOR	Abracon Part Number		 □ Documentation only □ ECN □ EOL 	⊠ Series □ Part Number	
Affected Revision:	New Revision:		Application:	Safety	
A	EC	DL		🛛 Non-Safety	
Prior to Change: Active https://abracon.com/Oscillators/ASTMK06	i.pdf				
After Change: EOL					
Cause/Reason for Change: Discontinuation of manufacturing capabilit	.у.				
	Chan	ge Plan			
Effective Date: 2/7/2022	Additional Remarks: N/A				
Change Declaration: N/A	·				
Issued Date:	Issued By:		Issued Department:		
2/7/2022	Brooke Cushman Product Engineer		Engineering		
Approval:			Approval:		
Thomas Culhane	Reuben Quintanilla		Ying Huang		
Engineering Director	Quality Director Purchasing Director				
	For Abrace	on EOL only			
Last Time Buy (if applicable): 5/7/2022	Alternate Part Number / Part Series:				
Additional Approval:	none				
Additional Approval.	Additional Approval:		Additional Approval:		
	Customer Appro	val (If Applicable)			
Qualification Status: <i>Note: It is considered approved if there is n</i>		□ Not accepted ustomer 1 month afte	r ECN/PCN is released.		
Customer Part Number:		Customer Project:	· .		
eustomer i urt Number.		customer roject.			
Company Name:	Company Representa	ative:	Representative Signature	:	
Customer Remarks:					

Form #7020 | Rev. G | Effective: 02/22/2021 |

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2.0 x 1.2 x 0.6mm

.63V 3.63V

.63V

ASTMK06

🚵 ESD Sensitive

Moisture Sensitivity Level (MSL) - 1

FEATURES:

- Ultra-miniature size: 2.0 x 1.2 x 0.6mm
- Supply Voltage: 1.5V to 3.63V
- Ultra-Low Current Consumption: 1.0µA typ.(no load)
- Frequency Stabilities include:

STANDARD SPECIFICATIONS:

- ± 75 ppm over -10 to $\pm 70^{\circ}$ C
- ± 100 ppm over -40 to +85°C
- Internal power supply filtering eliminates external bypass capacitor for Vdd port.

> **APPLICATIONS**:

(Pb)

• General Timekeeping

RoHS/RoHS II compliant

- Battery Management
- Portable devices
- RTC reference clock
- Bluetooth/WiFi modules

Parameters	Min	Тур	Max	Unit	Notes
Output Frequency (Fout)		32.768		kHz	
Initial Frequency Tolerance $(F_{init})^{(1)}$	-20		+20	ppm	T_A =+25°C, post reflow, V _{dd} :1.5-3.63V
Frequency Stability over Temperature $(F_{stab})^{(2)}$	-75 -100		+75 +100	ppm	T_A = -10°C to +70°C, V _{dd} :1.5-3.63V T_A = -40°C to +85°C, V _{dd} :1.5-3.63V
Aging (@+25°C)	-1		+1	ppm	First year
Supply Voltage (V _{dd})	1.5		3.63	V	T _A = over temperature
		1.0			T_A = +25°C, V _{dd} :1.5-3.63V. No load.
Current Consumption (I _{dd})			1.9	μA	T_A = -10°C to +70°C, V _{dd} max: 3.63V. No load
			2.2		T_A = -40°C to +85°C, V _{dd} max: 3.63V. No load.
Power Supply Ramp (tydd_Ramp)			100	ms	Over temperature, 0 to 90% V_{dd}
		180	300		$T_A = +25^{\circ}C \pm 10^{\circ}C$
Start-up Time at Power-up (T _{start})			450	ms	$T_A = -40^{\circ}C$ to $+70^{\circ}C$
			500		$T_A = +85^{\circ}C$
Operating Temperature Range (T _{use})	-10		+70	°C	Option "M"
	-40		+85	Ũ	Option "L"
LVCMOS Output (T _A = Over Temperatu	re. Typical val		= +25°C)		
Output Rise/Fall Time (t _r /t _f)		100	200	ns	10-90%, 15pF load, V_{dd} :1.5-3.63V
Output Clock Duty Cycle	48		52	%	
Output Voltage	90%*V _{dd}			V	V _{dd} :1.5-3.63V. I _{OH} = -10μA, 15pF
Output Voltage			$10\%*V_{dd}$	v	V _{dd} :1.5-3.63V. I _{OL} = 10µA, 15pF
Output Drive Level			50	pF	≥80% LVCMOS swing, V _{dd} :1.8V, 2.5V, 3.3V
Period Jitter (T _{jitt})		35		ns _{RMS}	Cycles – 10000, $T_A = +25^{\circ}C$

Note:

Measured peak-to-peak. Tested with Agilent 53132A frequency counter. Due to the low operating frequency, the gate time 1. must be ≥ 100 ms to ensure an accurate frequency measurement.

Measured peak-to-peak. Inclusive of initial tolerance at +25°C, and variations over operating temperature, rated power 2. supply voltage and load.



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(Pb) **RoHS/RoHS II compliant**

Absolute Maximum Ratings

Attempted operation outside the absolute maximum ratings may cause permanent damage to the part. Actual performance of the IC is only guaranteed within the operational specifications, not at absolute maximum ratings.

Parameters	Test Condition	Value	Unit		
Continuous Power Supply Voltage Range (V _{dd})		-0.5 to 3.63	V		
Short Duration Max. Power Supply Voltage (V _{dd})	≤ 30 minutes	4.0	V		
Short Duration Max. Operating Temperature Range	Vdd:1.5-3.63V, ≤30 minutes	125	°C		
Human Body Model (HBM) ESD Protection	JESD22-A114	3000	V		
Charge-Device Model (CDM) ESD Protection	JESD22-C101	750	V		
Machine Model (MM) ESD Protection	JESD22-A115	300	V		
Latch-up Tolerance	JESD78 Compliant				
Mechanical Shock Resistance	Mil 883, Method 2002	10000	g		
Mechanical Vibration Resistance	Mil 883, Method 2007	70	g		
2012 SMD Junction Temperature		150	°C		
Storage Temperature		-65 to +150	°C		

Block Diagram:





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TYPICAL PERFORMANCE DATA (TA=25°C, Vdd=1.8V, unless otherwise stated)





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OUTLINE DRAWING:



REFLOW PROFILE:



Item	Conditions			
T_S MAX to T_L (Ramp-up Rate)	3°C/second max			
Preheat	•			
Temperature Minimum (T _S MIN)	150°C			
Temperature Typical (T _S TYP)	175°C			
Temperature Maximum (T _S MAX)	200°C			
Time (t _s)	60 - 180 seconds			
Ramp-up Rate $(T_L \text{ to } T_P)$	3°C/second max			
Time Maintained Above				
Temperature (T _L)	217°C			
Time (t _L)	60 - 150 seconds			
Peak Temperature (T _P)	260°C max			
Target Peak Temperature (T _P Target)	255°C			
Time within 5°C of actual peak (t_P)	20-40 seconds			
Max. Number of Reflow Cycles	3			
Ramp-down Rate	6°C/second max			
Time 25°C to Peak Temperature (t)	8 minutes max			



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