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PCN Date: 8/7/2015		Effective Date: 9/11/2015					
Title: EFM8UB1x Revision C							
Originator: Kafai Leung	Phor 5232	ne: +1-512-532- 2	Dept: Marketing				
Customer Contact: Kathy Haggar	Phor	ne: +1-512-532-5261	Dept: Sales				
PCN Type:							
🛛 Datasheet							
Product Revision							
PCN Details							
Description of Change:   Silicon Labs is pleased to announce revision C of the EFM8UB1x devices and revision 1.0 of the corresponding datasheet for these products.   For customers using Revision B the change to Revision C eliminates a potential issue in USB device missing a token from the host and not responding to a transaction. This revision allows USB Low Energy Mode settings to be used with any power-saving options.   Revision C resolves the momentary current spike upon entering Shutdown mode.   In addition, for customers using Revision B the change to Revision C eliminates a potential issue with the Timer 3/4 32-bit counter not switching to the low frequency oscillator (LFOSCO) after entering Suspend mode if the system clock divider is set to a value of divide-by-4 or greater. This revision allows system clock divider to be at any value when entering Suspend mode.   Datasheet revision 1.0 updates the orderable part number to revision C along with other spec table edits in Table 4.1. Port I/O spec in Figure 4.6 and Figure 4.7 together with Table 4.13.   After the effective date of this PCN, Silicon Labs reserves the right to deliver EFM8UB1xFxG-C (Revision C) for customers ordering EFM8UB1xFxG-B (Revision B).   Reason for Change:							
EFM8UB1x Revision C release EFM8UB1x Datasheet revision 1.0 release							

W7206F5 Product Revision Notice Form rev E

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### Impact on Form, Fit, Function, Quality, Reliability:

There is no impact to form, fit, quality or reliability.

The following functions are impacted:

- The reset value of REVID SFR will read 0x03 for revision C instead of 0x02 for revision B or 0x01 for revision A.
- Behavior with USB Low Energy mode has been addressed.
- Behavior with momentary current spike upon entering Shutdown mode has been addressed.
- Behavior with Timer 3/4 at system when system clock divider value of divide-by-4 or
- greater has been addressed.

Product Identification:

Existing Part Number	Replacement Part Number	Drop in Compatible Indicator
EFM8UB10F8G-B-QFN20	EFM8UB10F8G-C-QFN20	Yes
EFM8UB10F8G-B-QFN20R	EFM8UB10F8G-C-QFN20R	Yes
EFM8UB10F16G-B-QFN28	EFM8UB10F16G-C-QFN28	Yes
EFM8UB10F16G-B-QFN28R	EFM8UB10F16G-C-QFN28R	Yes
EFM8UB10F16G-B-QFN20	EFM8UB10F16G-C-QFN20	Yes
EFM8UB10F16G-B-QFN20R	EFM8UB10F16G-C-QFN20R	Yes
EFM8UB11F16G-B-QSOP24	EFM8UB11F16G-C-QSOP24	Yes
EFM8UB11F16G-B-QSOP24R	EFM8UB11F16G-C-QSOP24R	Yes

Note: The part numbers above include tape and reel variants which are denoted with an "R" at the end of the orderable part number.

Last Date of Unchanged Product: 9/11/2015

#### Qualification Samples:

Samples are available now. Please contact your Silicon Labs sales representative to order samples. A list of Silicon Labs sales representatives is available at <u>www.silabs.com</u>.



Specific conditions of acceptance of this change will be considered on a case by case basis if written notice is submitted within 30 days of this notice. To request further data or inquire about this notification, please contact your local Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com.

In some cases rejection of a change notice may impact Silicon Labs product pricing, delivery, quality, or reliability.

**Customer Early Acceptance Sign Off:** 

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: Date: \_\_\_\_\_

Name: \_\_\_\_\_

Company:

Email your early Acceptance approval to: <u>katherine.haggar@silabs.com</u>

**Qualification Data:** 

See below.



# EFM8UB1\*/BB2\* AEC-Q100 Qualification Report

#### W7101F1 - Product Qualification Plan and Report Record Rev. G

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C8051F870 A2/A3/A4 HHGrace Fabrication, ASECL and UTL Assembly								
Test Name	Test Condition	Qualification	Start	Fail/Pass or End	Not es	Summ ary	Status	
Test Group A - Ad	ccelerated Environment Stress	Tests -20QFN -C	uPd Wire A	SECL				
HAST	AEC JESD22-A110		Q37190	0/77				
	130°C, 85%RH	3 lots, N=>77	Q37191	0/80	1	3 lots	Pass	
	Vcc=3.6V, 96 hours		Q37192	0/80		0/237		
Temp Cycle	AEC JESD22-A104		Q37196	0/80				
	Cond C: -65°C to 150°C	3 lots, N=>77	Q37197	0/80	1	3 lots	Pass	
	500 cycles		Q37198	0/80		0/240		
HTSL	AEC JESD22-A103		Q37193	0/30				
	150°C, 1000hr	3 lots, N=>25	Q37194	0/30	1	3 lots	Pass	
			Q37195	0/30		0/140		
JHAST	AEC JESD22-A118		Q37199	0/81		0,140		
2000	130°C, 85%RH	3 lots, N=>77	Q37200	0/80	1	3 lots	Pass	
	96hrs	5106,14-277	Q37200	0/82	,	0/243	r 033	
iest Gmun A - Ar	ccelerated Environment Stress	L Tests -280EN -0	-			0.240		
HAST	AEC JESD22-A110	100-20010-0	Q35792	0/80		I		
		2 John N 77	-		1	3 lots	D	
Tomo Anda	130°C,85%RH	3 lots, N=>77	Q35788	0/77	1		Pass	
	Vcc=3.6V, 96 hours		Q35789	0/80		0/237		
ſemp Cycle	AEC JESD22-A104		Q37160	0/80			_	
	Cond C: -65°C to 150°C	3 lots, N=>77	Q37161	0/80	1	3 lots	Pass	
	500 cycles		Q37162	0/80		0/240		
ITSL	AEC JESD22-A103		Q35682	0/30	1			
	150°C, 1000hr	3 lots, N=>25	Q35790	1/30	1,2	4 lots		
			Q37159	0/30	1			
			Q36555	0/30	1	1/120		
JHAST	AEC JESD22-A118		Q37163	0/80				
	130°C,85%RH	3 lots, N=>77	Q37164	0/80	1	3 lots	Pass	
	96hrs		Q37165	0/80		0/240		
'est Group A - A	ccelerated Environment Stress	Tests -24QSOP-0	աPd Wire L	ITL				
IAST	AEC JESD22-A110		Q36513	0/80				
	130°C,85%RH	3 lots, N=>77	Q36515	0/80	1	3 lots	Pass	
	Vcc=3.6V, 96 hours		Q36519	0/80		0/240		
'emp Cycl e	AEC JESD22-A104		Q36523	0/80				
	Cond C: -65°C to 150°C	3 lots, N=>77	Q36524	0/80	1	3 lots	Pass	
	500 cycles		Q36525	0/80		0/240		
HTSL	AEC JESD22-A103		Q36520	0/28				
	150°C, 1000hr	3 lots, N=>25	Q36521	0/28	1	3 lots	Pass	
			Q36522	0/28		0/84		
JHAST	AEC JESD22-A118		Q36522	0/80				
	130°C, 85%RH	3 lots, N=>77	0036527	0/80	1	3 lots	Pass	
	96hrs	0.000,14-777	Q036528	0/80	'	0/240	7033	
est Group B - M	ccelerated Lifetime Simulation	Tests	1 4000020	0,00		0.2-10		
	AEC JESD22-A108		035494	0.04				
ITOL		0.1-1-1-1-27	Q35684	0/84			D	
	125°C, Dynamic	3 lots, N=>77	Q35685	0/84		3 lots	Pass	
701	Vcc=3.6V, 1000 hours		Q37250	0/80		0/248		
.TOL	JA 108							

Approved by: Ramon Ponsones

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## EFM8UB1\*/BB2\* AEC-Q100 Qualification Report



W7101F1 - Product Qualification Plan and Report Record Rev. G

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C8051F870 A2	/A3/A4 HHGrace Fabr	ication, ASECL	and UTL	Assem bly			
	-40°C, Dynamic	1 lot, N=>32	Q36550	0/35		1 lots	Pass
	Vcc=3.6V, 1000 hours					0/35	
1:	AEC Q100-008		Q35681	0/839			
	125°C, Dynamic	3 lots, N=>800	Q36910	0/839		41ots	Pass
	Vcc=3.6V, 48 hours		Q37251	0/836		0/3354	
			Q36509	0/840			
Data Retention	AEC Q100-005		Q35781	0/45			
High Temp	150°C, 1000hrs	3 lots, N=>39	Q35783	0/44		3 lots	Pass
			Q37252	0/45		0/134	
Data Retention	AEC Q100-005		Q35784	0/45			
Low Temp	25°C, 1000hrs	3 lots, N=>38	Q35786	0/44		3 lots	Pass
			Q37253	0/45		0/134	
NVM P/E Cycling	AEC Q100-005		Q35787	0/84			
High Temp	85°C, 1000hrs	3 lots, N=>77	Q35782	0/84		3 lots	Pass
			Q37254	0/84		0/252	
NVM P/E Cycling	AEC Q100-005		Q35791	0/80			
Low temp	55°C, 1000hrs	3 lots, N=>77	Q35785	0/80		3 lots	Pass
			Q37255	0/84		0/244	
Test Group C - Pack	kage Assembly Integrity Te:	sts					
Wire Bond Pull	AEC Q003						
	Mil Std Method 2011	5 units, N=>30	Q37489	0/5		1lot	Pass
		28 QFN				0/5	
Wire Bond Pull	AEC Q003						
	Mil Std Method 2011	5 units, N=>30	Q37487	0/5		11ot	Pass
		20 QFN					
Wire Bond Pull	AEC Q003						
	Mil Std Method 2011	5 units, N=>30	Q037707	0/5		1lot	Pass
		24Q50P				0/5	
Test Group E - Elec		-					
ESD-HBM	AEC Q100-002		Q36561				2KV
		1 lot, N=>3	Q35689				2KV
			Q37643				2KV
ESD-MM	AEC Q100-003		Q37644				250 V
		1 lot, N=>3	Q35690				250 V
EED CDU	450.0400.011		00/705				4500.54
ESD-CDM	AEC Q100-011		Q36705		3		1500 V
		1 lot, N=>3	Q35688		4		1250 V
			Q37648		4		1250 V
			Q36558		4		1500 V
Latch Up	450 0100 004		Q36512	4250	5		1500 V
	AEC Q100-004 ±200mA	1 lot, N=>6	Q37647 Q37674	125C 25C			Pass Pass

Notes:

1. Parts are Pre-conditioned at MSL 2/260°C

2. 1 unit Failure

3. 20 QFN

Approved by: Ramon Ponsones

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Prepared on: 16-Jun-15

W7206F5 Product Revision Notice Form rev E

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## EFM8UB1\*/BB2\* AEC-Q100 Qualification Report

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C8051F870 A2/A3/A4 HHGrace Fabrication, ASECL and UTL Assembly

4, 28 QFN

5. 24 QSOP

This report applies to the following part numbers:

EFM8UB11F16G-B-QSOP24 EFM8UB11F16G-C-QFN24 EFM8UB10F16G-B-QFN28 EFM8UB10F16G-B-QFN20 EFM8UB10F8G-B-QFN20 EFM8BB22F16G-B-QFN28 EFM8BB21F16G-B-QSOP24 EFM8BB21F16G-B-QFN20