RFX Group
Incorporating Laptech Precision

OCXO Part No: OS240-1005-018

Issue 2; 6th May 2022

Features

- Temperature stability ±5ppb
- Low phase noise
- Frequency 10MHz
- Low pre-aged options available
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements

Option A

- Temperature stability: ±5ppb over (0 to +50)°C
- Output: Sinewave 0dBm
- Voltage: 12.0V
- Warm up current: 270mA
- Quiescent current: 120mA

Phase Noise (typical)

- FO_0+10Hz -125 dBc/Hz
- FO_0+100Hz -145 dBc/Hz
- FO_0+1KHz -155 dBc/Hz
- FO_0+10KHz -160 dBc/Hz
- FO_0+100KHz -165 dBc/Hz

Voltage / Load change

- ±5% supply voltage change: ±2ppb
- ±10% load change: ±10ppb

Ageing

After 30 days continuous operation:

- Per day: ±0.1ppb max.
- Per year: ±50ppb max.
- Warm up time: 2 minutes to within 0.1 ppm

Voltage Trim

- ±0.5ppm minimum
- Trim impedance 50KΩ

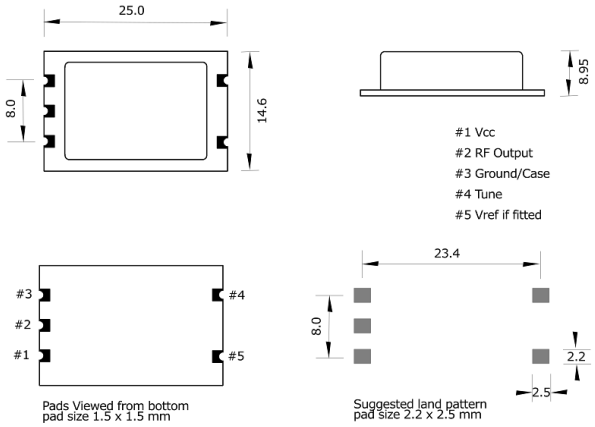
Reference Options

- 4.5V

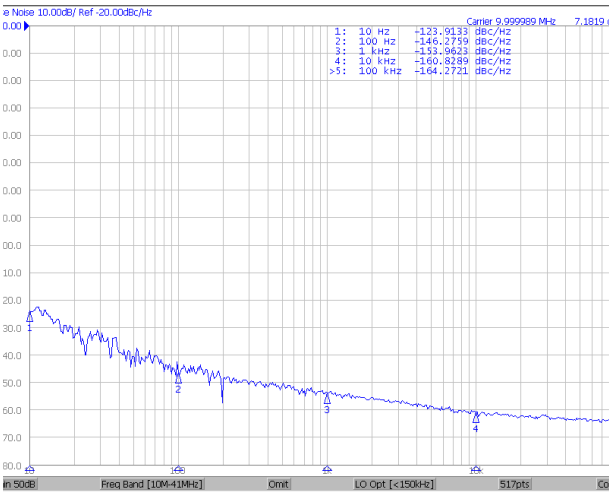
Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: -(40 to +125)°C
- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A

Dimensions (mm)



Phase Noise Plot



- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

Compliance

- RoHS Status (2011/65/EU) - Compliant
- REACH Status - Compliant

Packaging

- Pack Style: Bulk

Ordering Information

- Unique customer part number and custom specification issued with each application
- OCXO Part No: OS240-1005-018
- Frequency: 10MHz
- Stability/Output/Voltage : Option A
- Supply voltage code: V3= +12.0Vd.c. supply
- Add suffix (R) for Vref output on pin #5

Test Circuit - Sine