

### VS-1ENH01HM3, VS-1ENH02HM3

Vishay Semiconductors

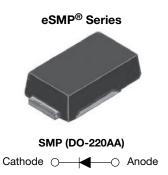
AUTOMOTIVE

RoHS

COMPLIANT HALOGEN

FREE

# Ultrafast Rectifier, 1 A FRED Pt®



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	1 A				
$V_{R}$	100 V, 200 V				
V <sub>F</sub> at I <sub>F</sub>	0.69 V				
I <sub>FSM</sub>	40 A				
t <sub>rr</sub> (typ.)	23 ns				
T <sub>J</sub> max.	175 °C				
Package	SMP (DO-220AA)				
Circuit configuration	Single				

#### **FEATURES**

- · Very low profile typical height of 1.0 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in high frequency, freewheeling, DC/DC converters, PFC, and in snubber, industrial, and automotive applications.

#### **MECHANICAL DATA**

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

Polarity: color band denotes cathode end

J-STD-002, meets JESD 201 class 2 whisker test

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse	VS-1ENH01HM3			100	V
voltage	VS-1ENH02HM3	$V_{RRM}$		200	V
Average rectified forward current		I <sub>F(AV)</sub>	T <sub>C</sub> = 168 °C	1	۸
Non-repetitive peak surge current		I <sub>FSM</sub>	$T_J = 25$ °C, 10 ms sine pulse	40	А
Operating junction and sto	rage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage,	VS-1ENH01HM3	V <sub>BR</sub> , V <sub>R</sub> I <sub>R</sub> = 100 μA		100	-	-	
blocking voltage	VS-1ENH02HM3			200	-	-	V
Forward voltage		V <sub>F</sub>	I <sub>F</sub> = 1 A	-	0.86	0.92	V
			I <sub>F</sub> = 1 A, T <sub>J</sub> = 150 °C	-	0.69	0.74	
Reverse leakage current		I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	-	2	μA
			$T_J = 150 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	-	20	
Junction capacitance		C <sub>T</sub>	V <sub>R</sub> = 200 V	-	8	-	pF

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	23	-	
Reverse recovery time	t <sub>rr</sub>	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	28	]
		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 1 A dI <sub>F</sub> /dt = 200 A/µs	-	14	-	ns A nC
		T <sub>J</sub> = 125 °C		-	22	-	
Peak recovery current I <sub>RRM</sub>		T <sub>J</sub> = 25 °C		-	1.7	-	
	IRRM	T <sub>J</sub> = 125 °C	V <sub>R</sub> = 100 V	-	2.7	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	10	-	
		T <sub>J</sub> = 125 °C		=	29	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature		T <sub>J</sub> , T <sub>Stg</sub>		-55	_	175	°C
range		J, Stg		-55	_	173	O
Thermal resistance, junction to mount		R <sub>thJM</sub> <sup>(1)</sup>	Infinite heatsink	-	7	9	°C/W
Thermal resistance, junction to ambient		R <sub>thJA</sub>	PCB footprint 4.8 mm x 4.8 mm	-	107	-	C/VV
Approximate weight				0.024		g	
Marking daving	VS-1ENH01HM3	Consist de SMP (DO 2004A)		1H1			
Marking device	VS-1ENH02HM3		Case style SMP (DO-220AA)	1H2			

#### Note

<sup>(1)</sup> Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

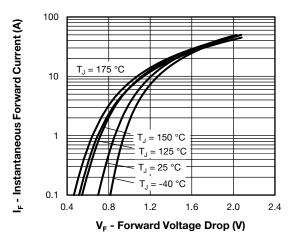


Fig. 1 - Typical Forward Voltage Drop Characteristics

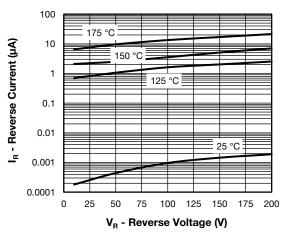


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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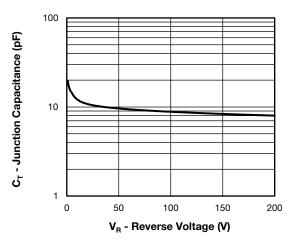


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

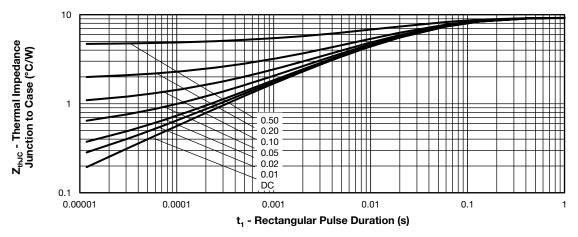


Fig. 4 - Transient Thermal Impedance, Junction to Case

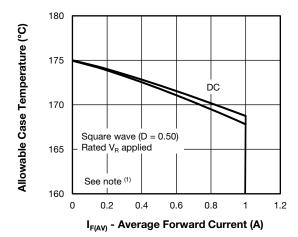


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

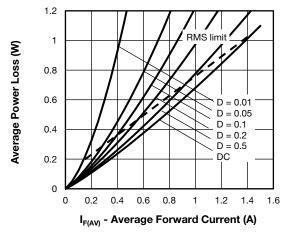


Fig. 6 - Forward Power Loss Characteristics

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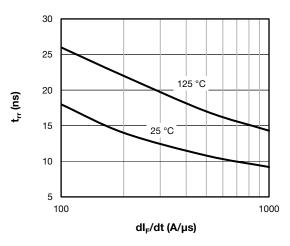


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

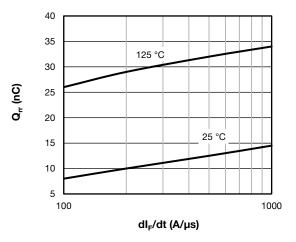
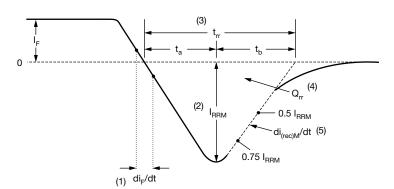


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; \ I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{F}$  to point where a line passing through 0.75  $I_{RRM}$  and 0.50  $I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

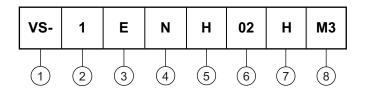
Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (1 = 1 A)

Circuit configuration:

E = single diode

4 - N = SMP package

5 - Process type,

H = ultrafast recovery

6 - Voltage code (02 = 200 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-1ENH01HM3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH01HM3/85A	85A	10 000	13" diameter plastic tape and reel			
VS-1ENH02HM3/84A	84A	3000	7" diameter plastic tape and reel			
VS-1ENH02HM3/85A	85A	10 000	13" diameter plastic tape and reel			

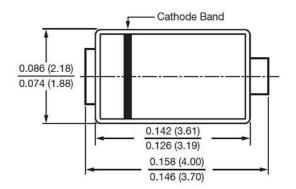
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96547</u>					
Part marking information	www.vishay.com/doc?96574				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96550				

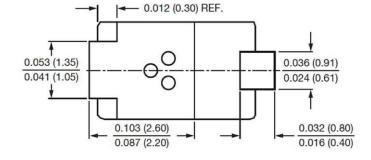


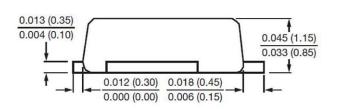
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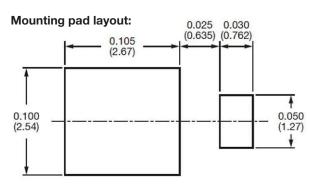
# **SMP (DO-220AA)**

### **DIMENSIONS** in inches (millimeters)











### **Legal Disclaimer Notice**

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