

## LOW POWER AND LOW OFFSET VOLTAGE SUPER SMALL-SIZED SINGLE C-MOS OPERATIONAL AMPLIFIER

### ■GENERAL DESCRIPTION

The **NJU7007/08** are super small-sized package single C-MOS operational amplifiers operated on a single-power-supply, low power, low offset voltage and low operating current.

The input offset voltage is lower than 4mV, and the input bias current is as low as than 1pA, consequently very small signal around the ground level can be amplified.

The minimum operating voltage is 1V and the output stage permits output signal to swing between both of the supply rails.

Furthermore, The **NJU7007/08** are packaged with super small-sized SC88A, therefore it can be especially applied to portable items.

### ■FEATURES

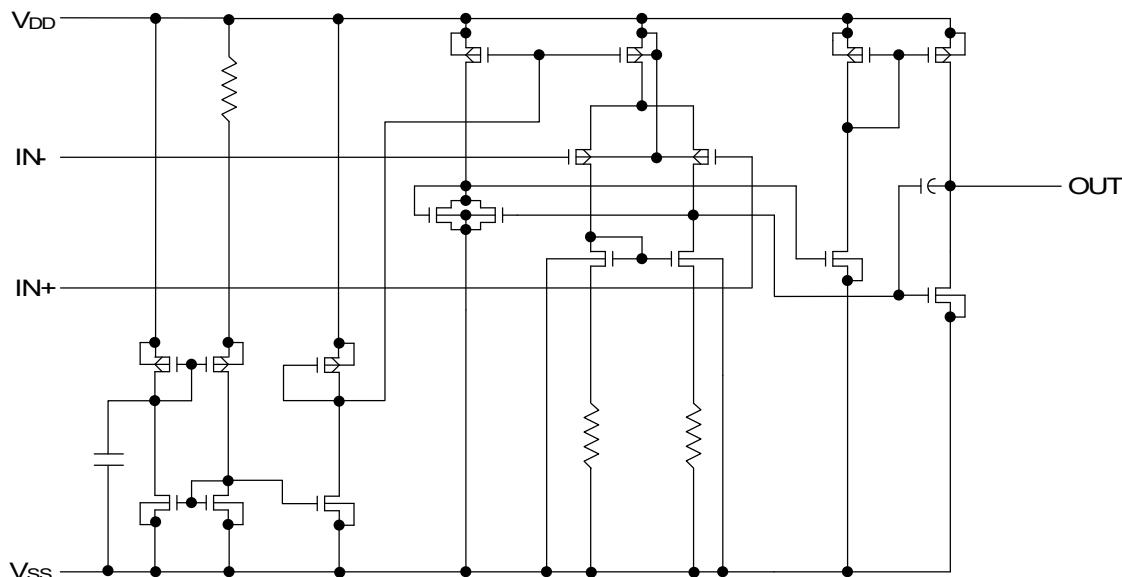
- Low Offset Voltage  $V_{IO}=4\text{mV}$  max
- Single Low Power Supply  $V_{DD}=1.0\sim 5.5\text{V}$
- Wide Output Swing Range  $V_{OM}=2.9\text{V}$  min @  $V_{DD}=3.0\text{V}$
- Low Operating Current (See Line-up)
- Low Bias Current  $I_{IB}=1\text{pA}$  typ
- Compensation Capacitor Incorporated
- Package Outline SC88A
- C-MOS Technology

### ■LINE-UP

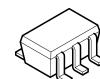
$(V_{DD}=3.0\text{V}, Ta=25^\circ\text{C})$

PARAMETER	NJU7007	NJU7008	UNIT
Operating Current	15	200	$\mu\text{A}$ (typ)
Slew Rate	0.1	2.4	$\text{V}/\mu\text{s}$ (typ)
Unity Gain Bandwidth	0.2	1.0	MHz(typ)

### ■EQUIVALENT CIRCUIT

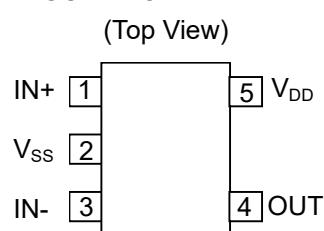


### ■PACKAGE INFORMATION



**NJU7007F3**  
**NJU7008F3**

### ■PIN CONFIGURATION



## ■ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>DD</sub>	7.0	V
Differential Input Voltage	V <sub>ID</sub>	±7.0 (Note1)	V
Common Mode Input Voltage	V <sub>IC</sub>	-0.3~7.0	V
Power Dissipation	P <sub>D</sub>	250 (Note2)	mW
Operating Temperature	T <sub>opr</sub>	-40~+85	°C
Storage Temperature	T <sub>stg</sub>	-55~+125	°C

Note1) If the supply voltage (V<sub>DD</sub>) is less than 7.0V, the input voltage must not over the V<sub>DD</sub> level though 7.0V is limit specified.

Note2) The power dissipation is value mounted on a glass epoxy board (FR-4) in size of 114.3x76.2x1.6 mm.

Note3) Decoupling capacitor should be connected between V<sub>DD</sub> and V<sub>SS</sub> due to the stabilized operation for the circuit.

## ■ELECTRICAL CHARACTERISTICS

NJU7007

(V<sub>DD</sub>=3.0V, R<sub>L</sub>=∞, Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V <sub>IO</sub>	V <sub>IN</sub> =V <sub>DD</sub> /2	-	-	4	mV
Input Offset Current	I <sub>IO</sub>		-	1	-	pA
Input Bias Current	I <sub>IB</sub>		-	1	-	pA
Input Impedance	R <sub>IN</sub>		-	1	-	TΩ
Large Signal Voltage Gain	A <sub>VD</sub>		60	70	-	dB
Input Common Mode Voltage Range	V <sub>ICM</sub>		0~2.5	-	-	V
Maximum Output Swing Voltage	V <sub>OM1</sub>	R <sub>L</sub> =1MΩ	V <sub>DD</sub> -0.1	-	-	V
	V <sub>OM2</sub>	R <sub>L</sub> =1MΩ	-	-	V <sub>SS</sub> +0.1	V
Output Source Current	I <sub>OH</sub>	V <sub>O</sub> =V <sub>DD</sub> /2	7	12		μA
Common Mode Rejection Ratio	CMR	V <sub>IN</sub> =V <sub>DD</sub> /2	55	65	-	dB
Supply Voltage Rejection Ratio	SVR	V <sub>DD</sub> =1.5~5.5V	60	70	-	dB
Operating Current	I <sub>DD</sub>		-	15	25	μA
Slew Rate	SR		-	0.1	-	V/μs
Unity Gain Bandwidth	F <sub>t</sub>	A <sub>v</sub> =40dB,C <sub>L</sub> =10pF	-	0.2	-	MHz

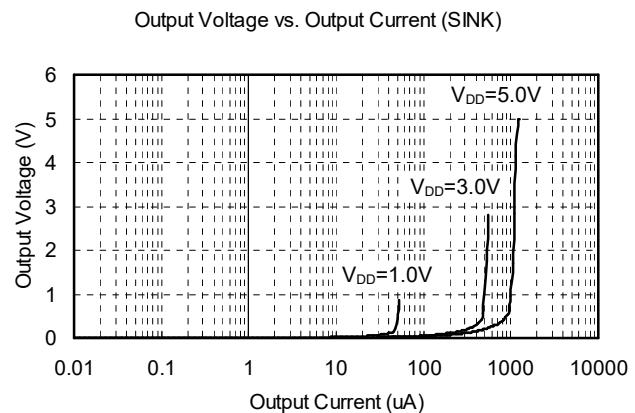
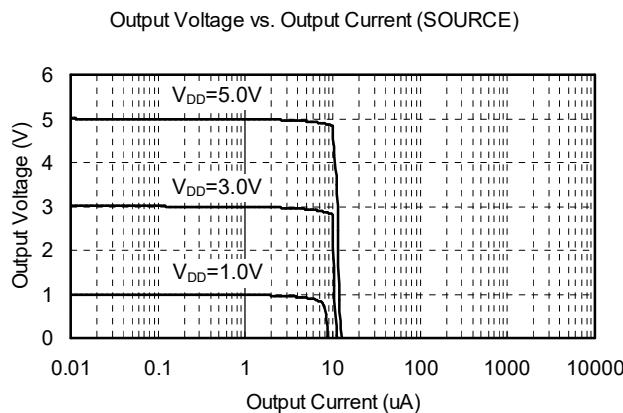
NJU7008

(V<sub>DD</sub>=3.0V, R<sub>L</sub>=∞, Ta=25°C)

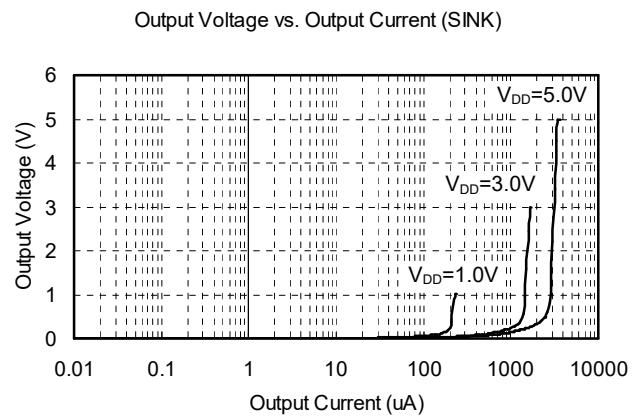
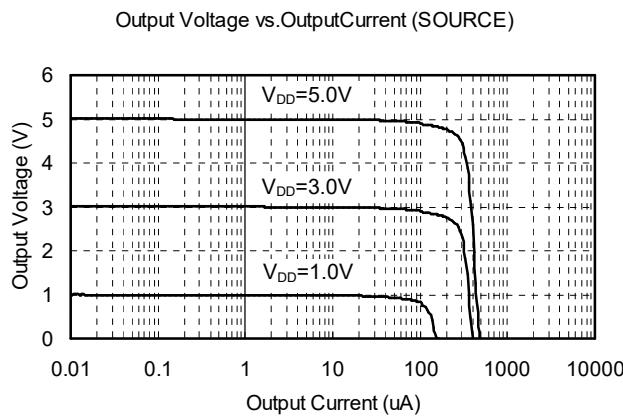
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V <sub>IO</sub>	V <sub>IN</sub> =V <sub>DD</sub> /2	-	-	4	mV
Input Offset Current	I <sub>IO</sub>		-	1	-	pA
Input Bias Current	I <sub>IB</sub>		-	1	-	pA
Input Impedance	R <sub>IN</sub>		-	1	-	TΩ
Large Signal Voltage Gain	A <sub>VD</sub>		60	70	-	dB
Input Common Mode Voltage Range	V <sub>ICM</sub>		0~2.5	-	-	V
Maximum Output Swing Voltage	V <sub>OM1</sub>	R <sub>L</sub> =50kΩ	V <sub>DD</sub> -0.1	-	-	V
	V <sub>OM2</sub>	R <sub>L</sub> =50kΩ	-	-	V <sub>SS</sub> +0.1	V
Output Source Current	I <sub>OH</sub>	V <sub>O</sub> =V <sub>DD</sub> /2	100	200	-	μA
Common Mode Rejection Ratio	CMR	V <sub>IN</sub> =V <sub>DD</sub> /2	55	65	-	dB
Supply Voltage Rejection Ratio	SVR	V <sub>DD</sub> =1.5~5.5V	60	70	-	dB
Operating Current	I <sub>DD</sub>		-	200	400	μA
Slew Rate	SR		-	2.4	-	V/μs
Unity Gain Bandwidth	F <sub>t</sub>	A <sub>v</sub> =40dB,C <sub>L</sub> =10pF	-	1.0	-	MHz

## ■TYPICAL CHARACTERISTICS

## (1)NJU7007



## (2)NJU7008



[CAUTION]  
The specifications on this data book are only given for information , without any guarantee as regards either mistakes or omissions.  
The application circuits in this data book are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.