

MOSFET - Power, N-Channel, SUPERFET III, FRFET

650 V, 95 mΩ, 36 A
NVHL095N65S3HF

Description

SUPERFET III MOSFET is onsemi's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET is very suitable for the various power system for miniaturization and higher efficiency.

SUPERFET III HF version provides fast recovery for improved efficiency in high speed switching applications.

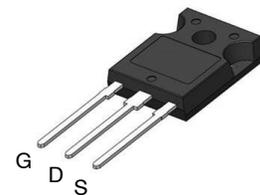
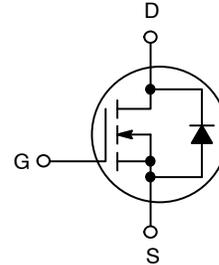
Features

- 700 V @ $T_J = 150^\circ\text{C}$
- Typ. $R_{DS(on)} = 78\text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 66\text{ nC}$)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)} = 556\text{ pF}$)
- 100% Avalanche Tested
- NVHL Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Automotive On Board Charger HEV-EV
- Automotive DC/DC Converter for HEV-EV

V_{DSS}	$R_{DS(ON)}\text{ MAX}$	$I_D\text{ MAX}$
650 V	95 mΩ @ 10 V	36 A



TO-247 Long Leads
CASE 340CX

MARKING DIAGRAM



A = Assembly Plant Code
YWW = Data Code (Year & Week)
ZZ = Assembly Lot Code
NVHL095N65S3HF = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

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ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{DSS}	Drain-to-Source Voltage	650	V
V _{GSS}	Gate-to-Source Voltage	- DC	±30
		- AC (f > 1 Hz)	±30
I _D	Drain Current	- Continuous (T _C = 25°C)	36
		- Continuous (T _C = 100°C)	22.8
I _{DM}	Drain Current	- Pulsed (Note 1)	90
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	440	mJ
I _{AS}	Avalanche Current (Note 2)	4.6	A
E _{AR}	Repetitive Avalanche Energy (Note 1)	2.72	mJ
dv/dt	MOSFET dv/dt	100	V/ns
	Peak Diode Recovery dv/dt (Note 3)	50	
P _D	Power Dissipation	(T _C = 25°C)	272
		- Derate Above 25°C	2.176
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. I_{AS} = 4.6 A, R_G = 25 Ω, starting T_J = 25°C.

3. I_{SD} ≤ 18 A, di/dt ≤ 200 A/μs, V_{DD} ≤ 400 V, starting T_J = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case, Max.	0.46	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient, Max.	40	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NVHL095N65S3HF	NVHL095N65S3HF	TO-247	Tube	N/A	N/A	30 Units

NVHL095N65S3HF

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain-to-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650	-	-	V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700	-	-	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 15 mA, Referenced to 25°C	-	0.63	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	-	-	10	μA
		V _{DS} = 520 V, T _C = 125°C	-	11	-	
I _{GSS}	Gate-to-Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V	-	-	±100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 0.86 mA	3.0	-	5.0	V
R _{DS(on)}	Static Drain-to-Source On Resistance	V _{GS} = 10 V, I _D = 18 A	-	78	95	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 18 A	-	19	-	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz	-	3105	-	pF
C _{oss}	Output Capacitance		-	65	-	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	556	-	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	107	-	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 18 A, V _{GS} = 10 V (Note 4)	-	66	-	nC
Q _{gs}	Gate-to-Source Gate Charge		-	22	-	nC
Q _{gd}	Gate-to-Drain "Miller" Charge		-	26	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	2.4	-	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 400 V, I _D = 18 A, V _{GS} = 10 V, R _g = 4.7 Ω (Note 4)	-	29.7	-	ns
t _r	Turn-On Rise Time		-	24.7	-	ns
t _{d(off)}	Turn-Off Delay Time		-	74.6	-	ns
t _f	Turn-Off Fall Time		-	3.2	-	ns

SOURCE-DRAIN DIODE CHARACTERISTICS

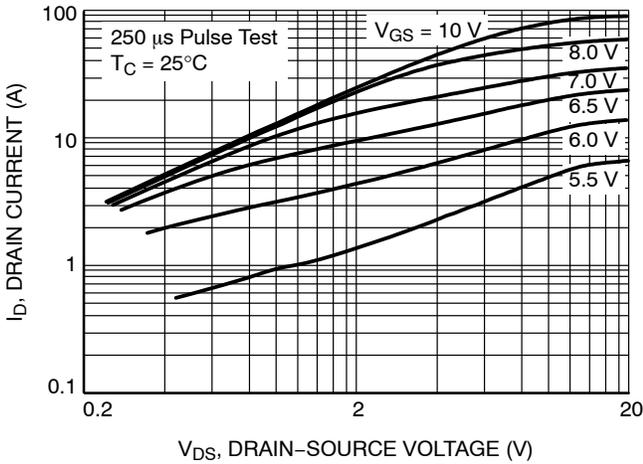
I _S	Maximum Continuous Source-to-Drain Diode Forward Current	-	-	36	A	
I _{SM}	Maximum Pulsed Source-to-Drain Diode Forward Current	-	-	90	A	
V _{SD}	Source-to-Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 18 A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 18 A, dI _F /dt = 100 A/μs	-	91	-	ns
Q _{rr}	Reverse Recovery Charge		-	363	-	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

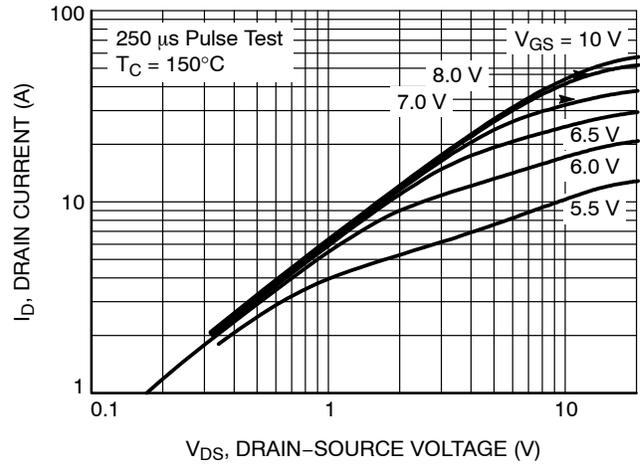
4. Essentially independent of operating temperature typical characteristics.

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TYPICAL CHARACTERISTICS



**Figure 1. On-Region Characteristics
25°C**



**Figure 2. On-Region Characteristics
150°C**

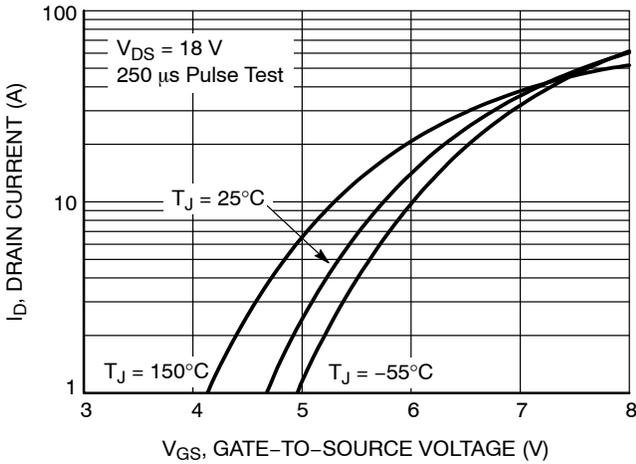
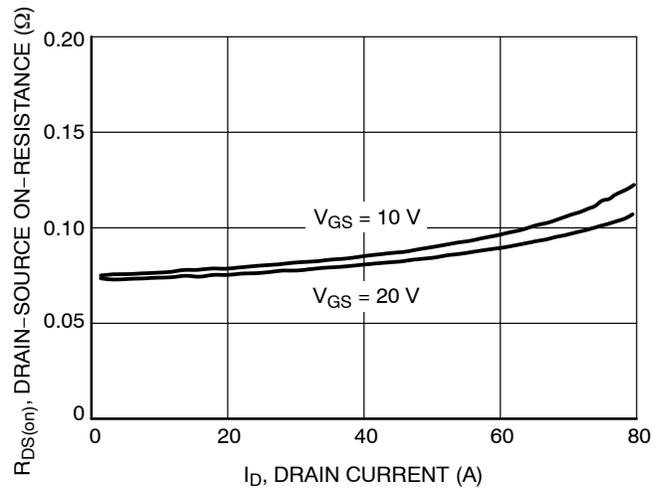
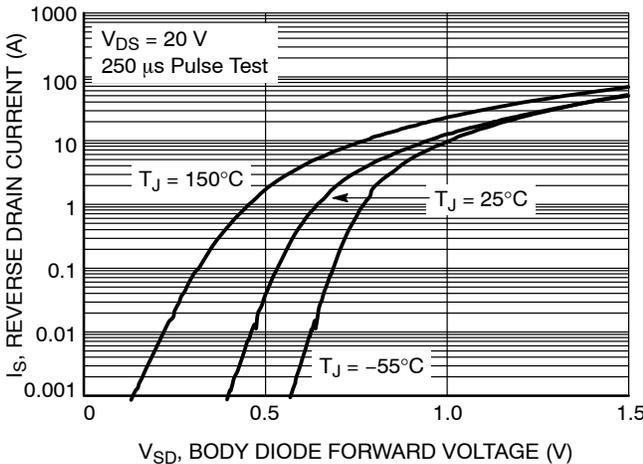


Figure 3. Transfer Characteristics



**Figure 4. On-Resistance Variation vs. Drain
Current and Gate Voltage**



**Figure 5. Body Diode Forward Voltage
Variation vs. Source Current and Temperature**

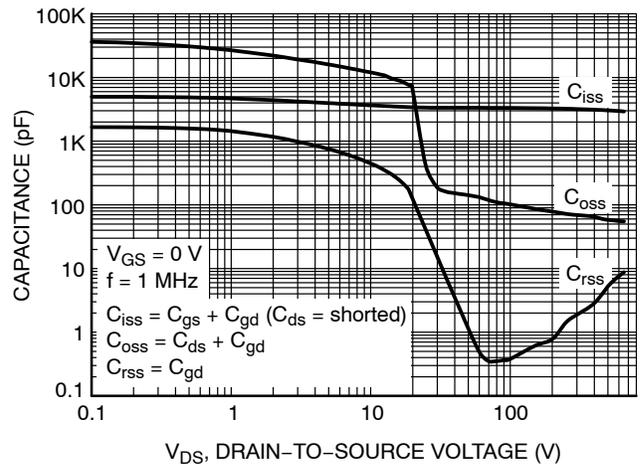


Figure 6. Capacitance Characteristics

TYPICAL CHARACTERISTICS

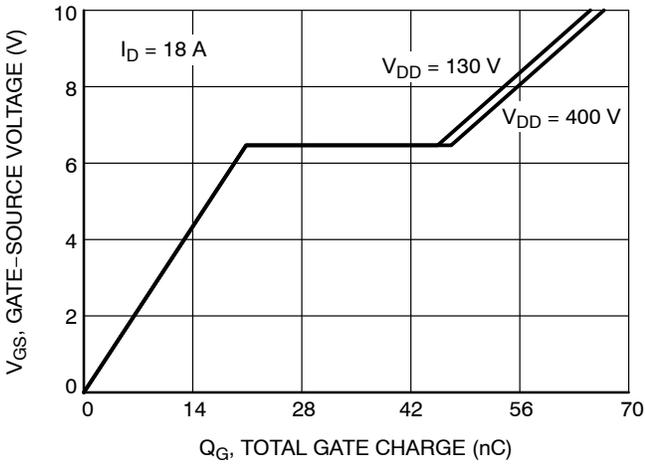


Figure 7. Gate Charge Characteristics

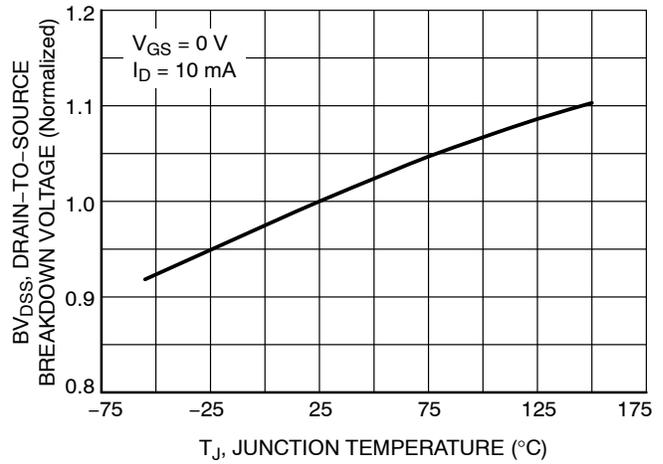


Figure 8. Breakdown Voltage Variation vs. Temperature

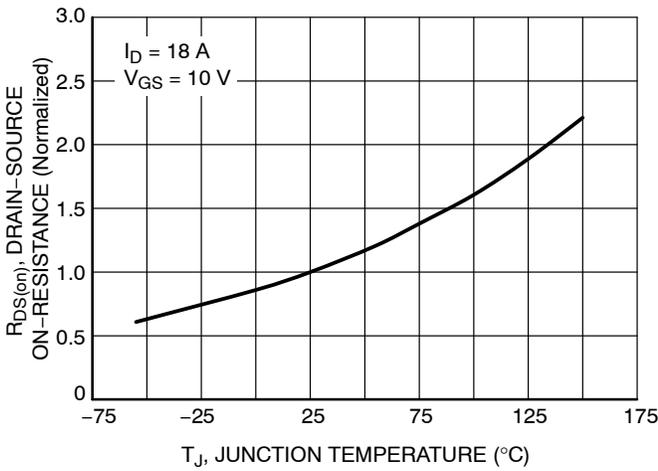


Figure 9. On-Resistance Variation vs. Temperature

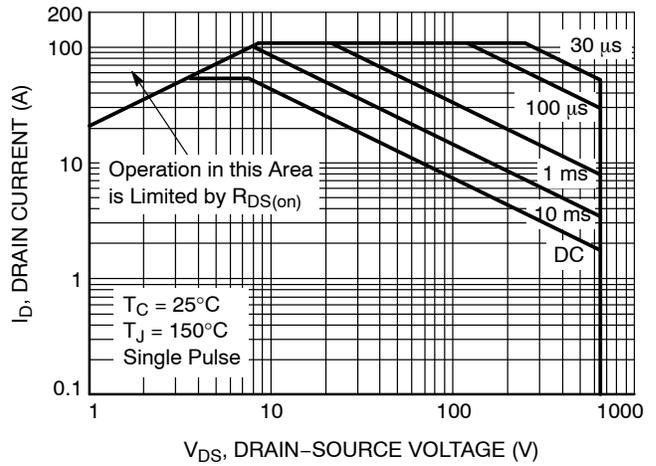


Figure 10. Maximum Safe Operating Area

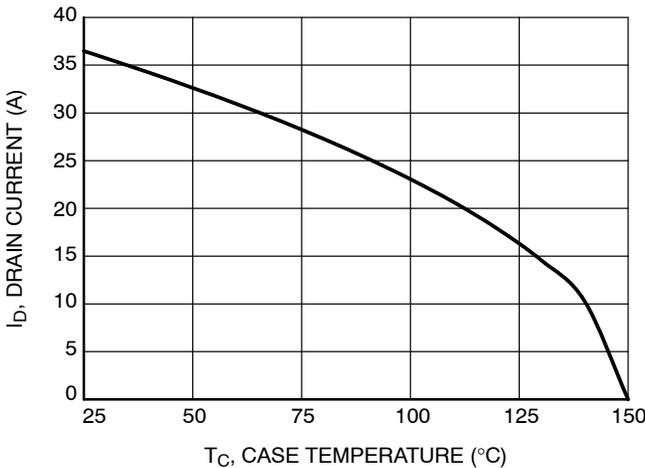


Figure 11. Maximum Drain Current vs. Case Temperature

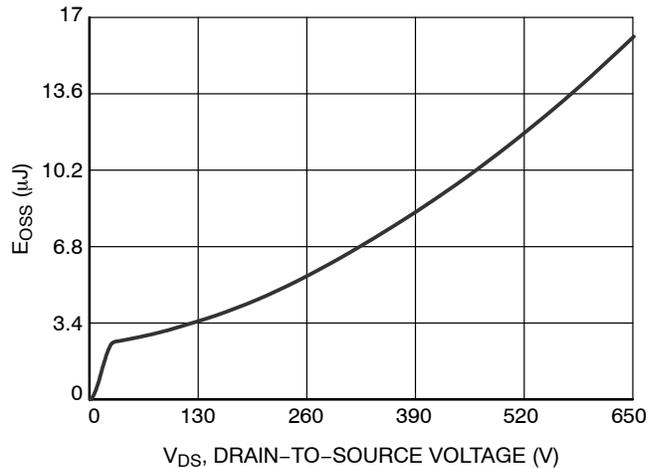


Figure 12. E_{OSS} vs. Drain-to-Source Voltage

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TYPICAL CHARACTERISTICS

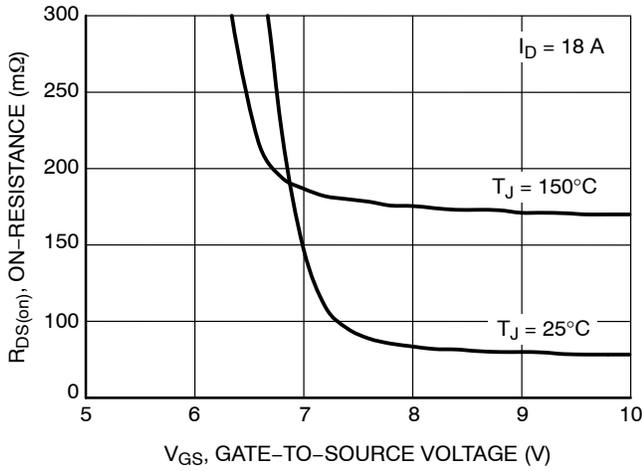


Figure 13. $R_{DS(on)}$ vs. Gate Voltage

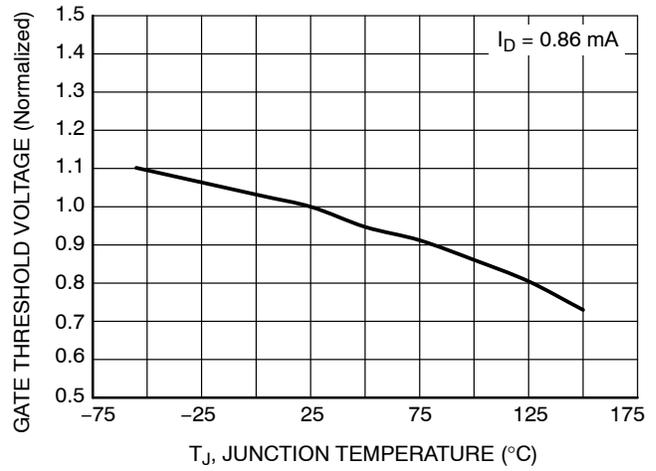


Figure 14. Normalized Gate Threshold Voltage vs. Temperature

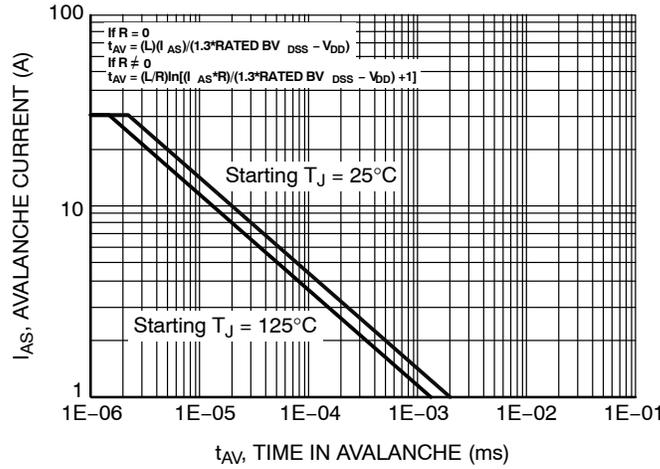


Figure 15. Unclamped Inductive Switching Capability

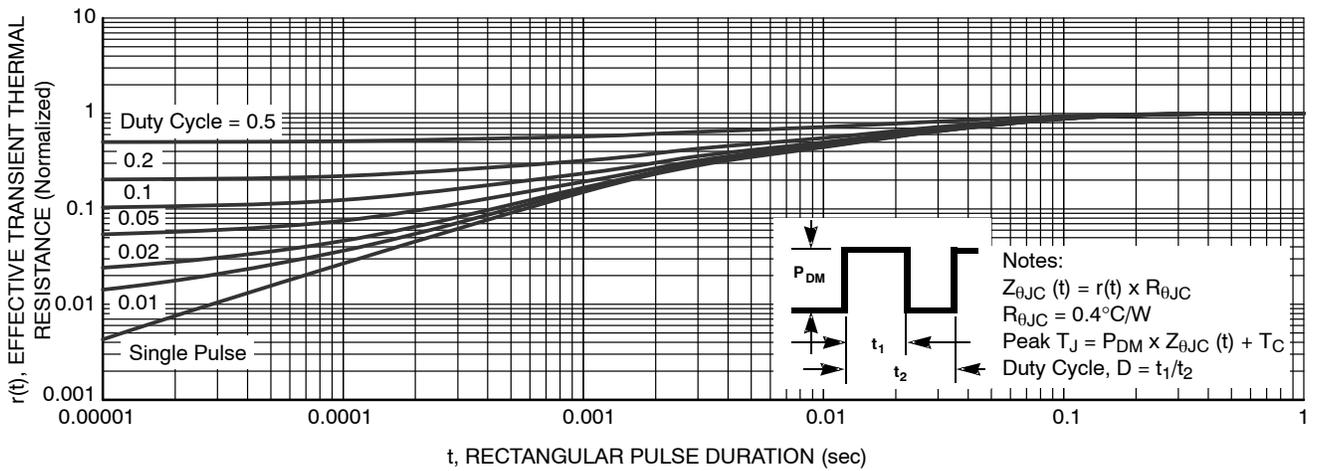


Figure 16. Transient Thermal Response

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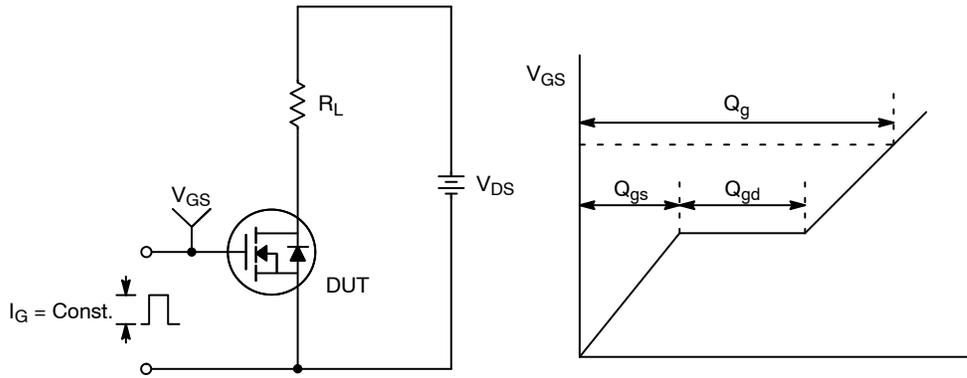


Figure 17. Gate Charge Test Circuit & Waveform

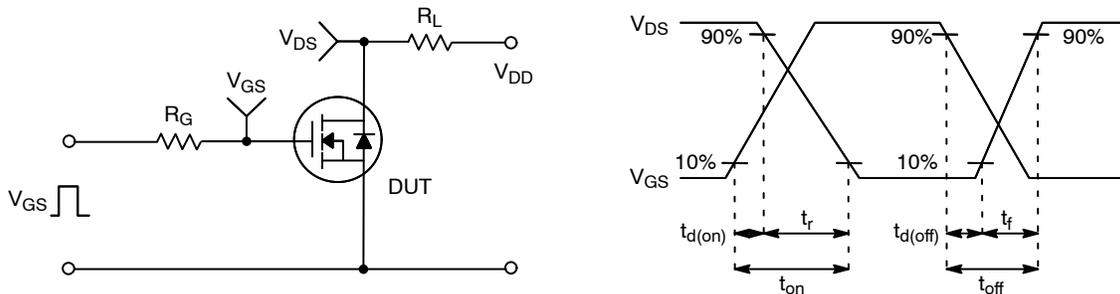


Figure 18. Resistive Switching Test Circuit & Waveforms

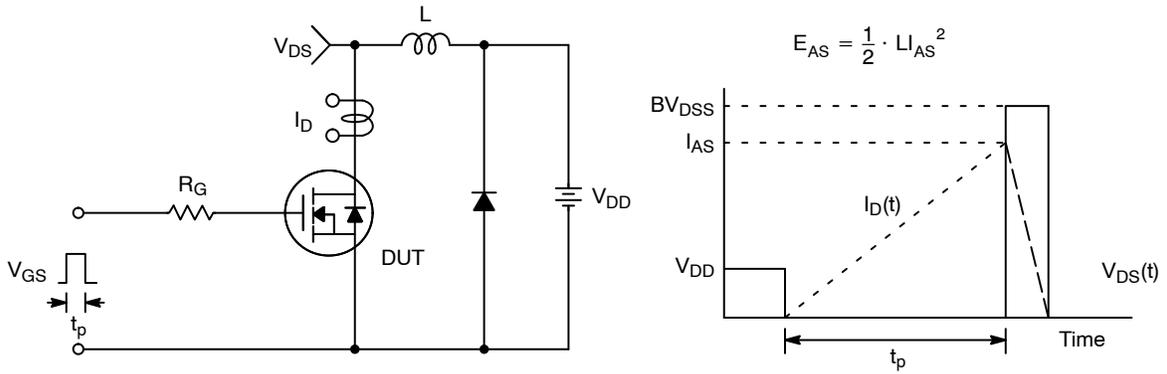


Figure 19. Unclamped Inductive Switching Test Circuit & Waveforms

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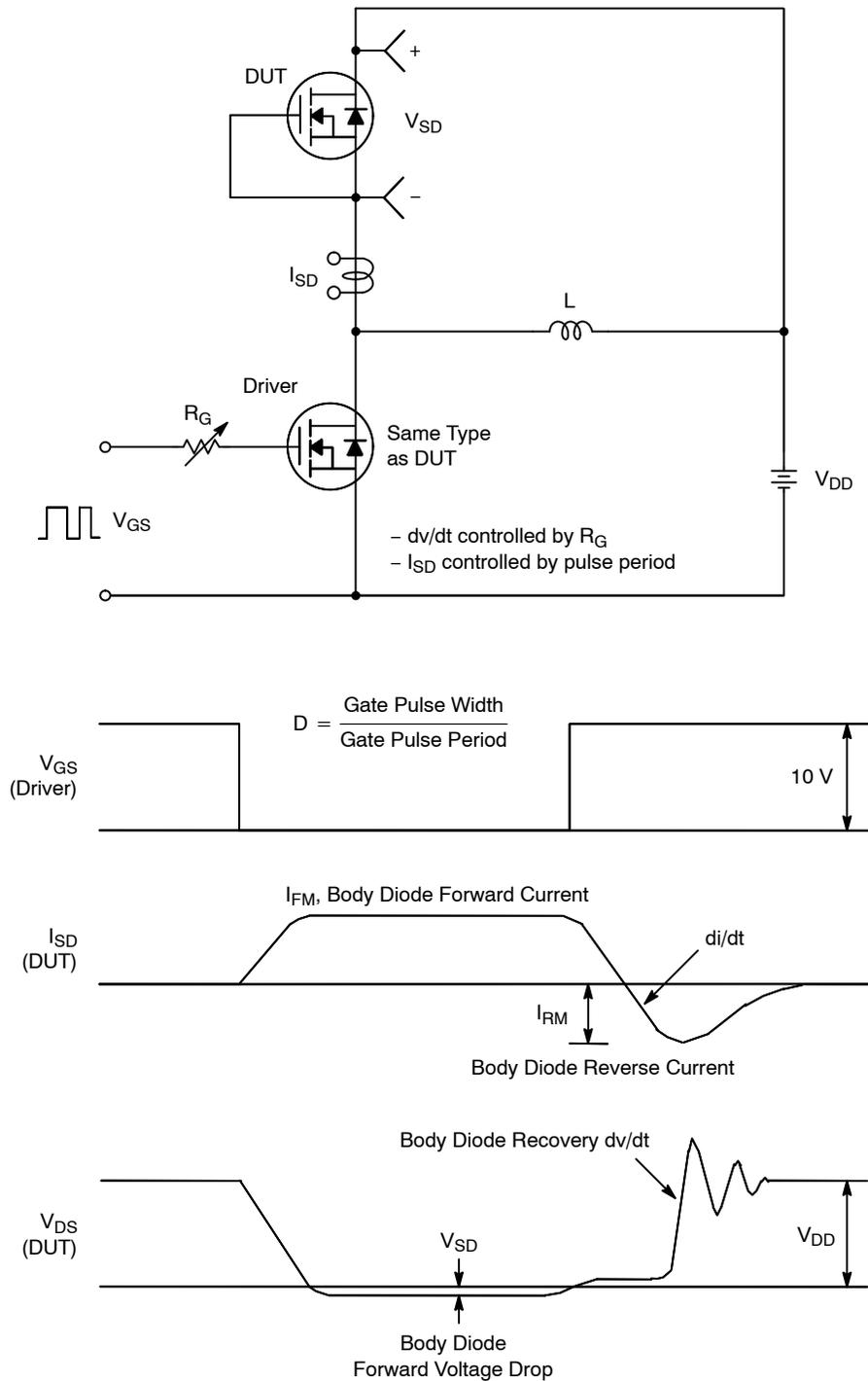


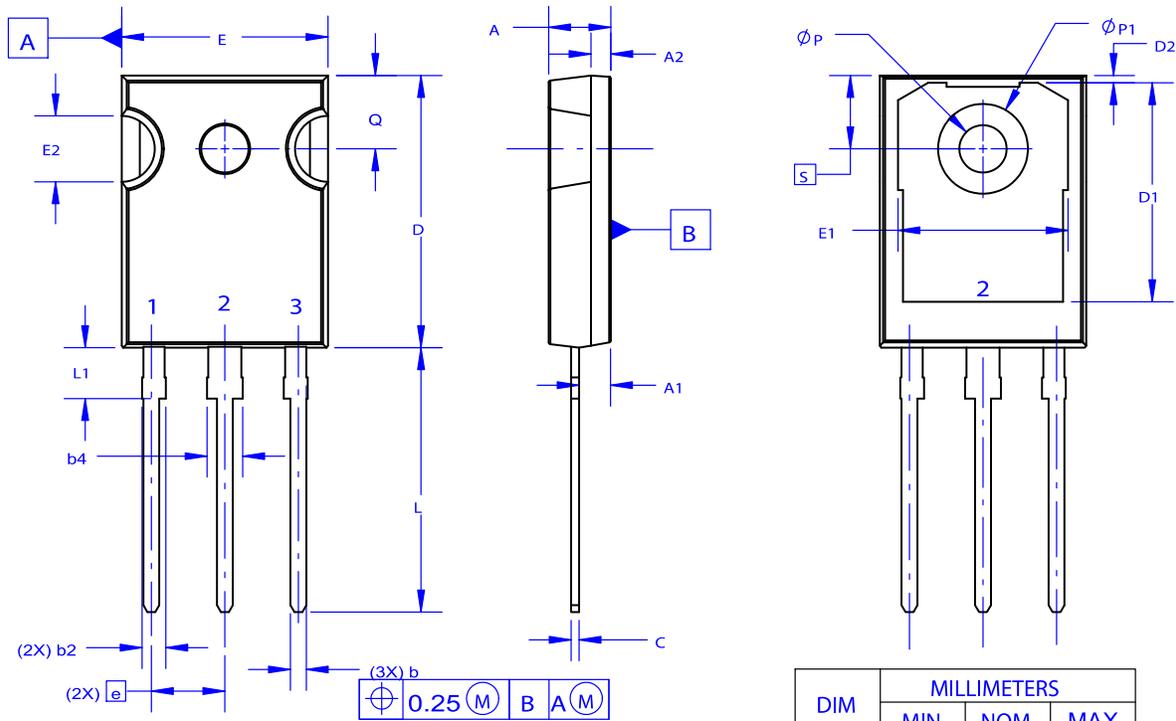
Figure 20. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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PACKAGE DIMENSIONS

TO-247-3LD
CASE 340CX
ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

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