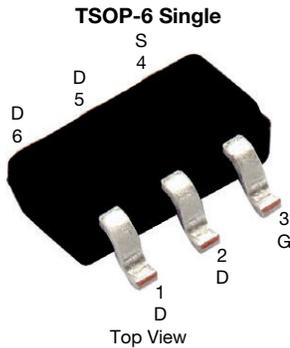


N-Channel 40 V (D-S) MOSFET



Marking code: AW

PRODUCT SUMMARY	
V_{DS} (V)	40
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10$ V	0.0355
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5$ V	0.0425
Q_g typ. (nC)	5.3
I_D (A) ^a	7.4
Configuration	Single

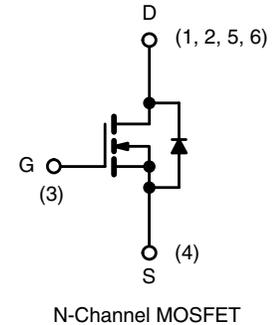
FEATURES

- TrenchFET[®] power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE
 Available

APPLICATIONS

- DC/DC Converter



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free	Si3438DV-T1-E3
Lead (Pb)-free and halogen-free	Si3438DV-T1-GE3

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	40	V
Gate-source voltage		V_{GS}	± 20	
Continuous drain current ($T_J = 150$ °C)	$T_C = 25$ °C	I_D	7.4	A
	$T_C = 70$ °C		5.8	
	$T_A = 25$ °C		5.5 ^{b, c}	
	$T_A = 70$ °C		4.4 ^{b, c}	
Pulsed drain current		I_{DM}	20	
Continuous source-drain diode current	$T_C = 25$ °C	I_S	2.9	
	$T_A = 25$ °C		1.6 ^{b, c}	
Maximum power dissipation	$T_C = 25$ °C	P_D	3.5	W
	$T_C = 70$ °C		2.2	
	$T_A = 25$ °C		2 ^{b, c}	
	$T_A = 70$ °C		1.25 ^{b, c}	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^{b, d}	$t \leq 5$ s	R_{thJA}	50	62.5	°C/W
Maximum junction-to-foot	Steady state	R_{thJF}	28	35	

Notes

- Based on 25 °C
- Surface mounted on 1" x 1" FR4 board
- $t = 5$ s
- Maximum under steady state conditions is 110 °C/W



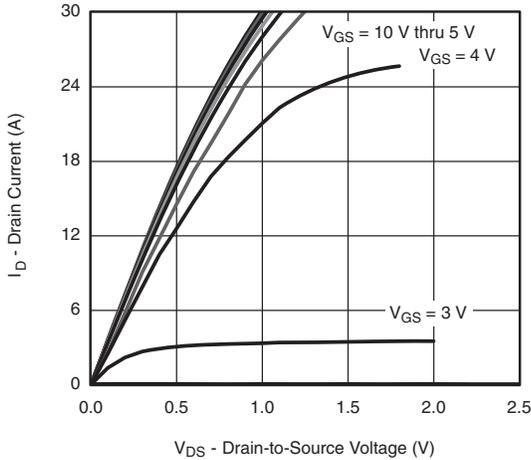
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	44	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-5.5	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.4	-	3	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
On-state drain current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	10	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A	-	0.0295	0.0355	Ω
		V _{GS} = 4.5 V, I _D = 4 A	-	0.0355	0.0425	
Forward transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 5 A	-	22	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz	-	640	-	pF
Output capacitance	C _{oss}		-	73	-	
Reverse transfer capacitance	C _{rss}		-	41	-	
Total gate charge	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 5 A	-	11.7	20	nC
		V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 5 A	-	5.3	9	
Gate-source charge	Q _{gs}		-	1.9	-	
Gate-drain charge	Q _{gd}	-	1.7	-		
Gate resistance	R _g	f = 1 MHz	0.45	2.2	4.4	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 20 V, R _L = 4 Ω I _D ≅ 5 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	16	30	ns
Rise time	t _r		-	17	35	
Turn-off delay time	t _{d(off)}		-	16	30	
Fall time	t _f		-	10	20	
Turn-on delay time	t _{d(on)}	V _{DD} = 20 V, R _L = 4 Ω I _D ≅ 5 A, V _{GEN} = 10 V, R _g = 1 Ω	-	7	14	
Rise time	t _r		-	10	20	
Turn-off delay time	t _{d(off)}		-	15	30	
Fall time	t _f		-	9	18	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	2.9	A
Pulse diode forward current	I _{SM}		-	-	20	
Body diode voltage	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V	-	0.78	1.2	V
Body diode reverse recovery time	t _{rr}	I _F = 3 A, di/dt = 100 A/μs, T _J = 25 °C	-	19	35	ns
Body diode reverse recovery charge	Q _{rr}		-	14	30	nC
Reverse recovery fall time	t _a		-	13	-	ns
Reverse recovery rise time	t _b		-	6	-	

Notes

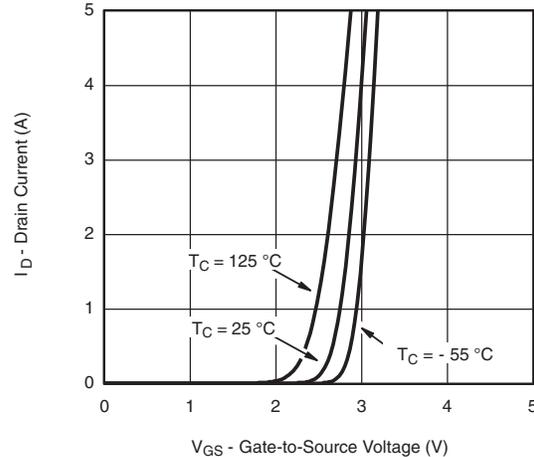
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

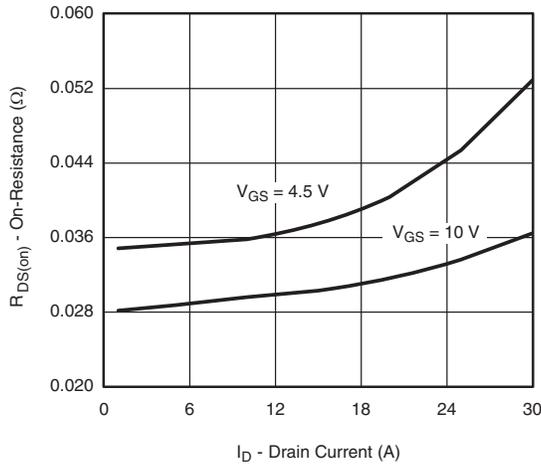
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



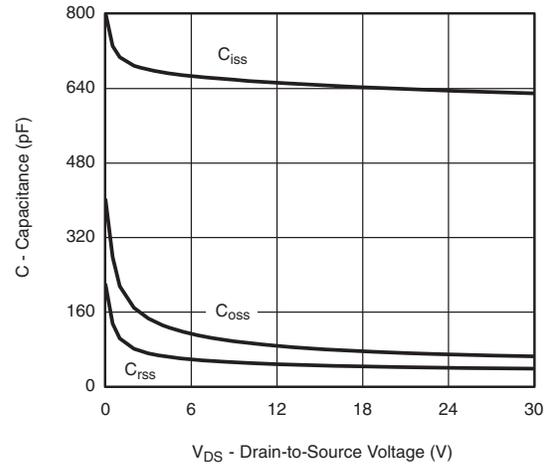
Output Characteristics



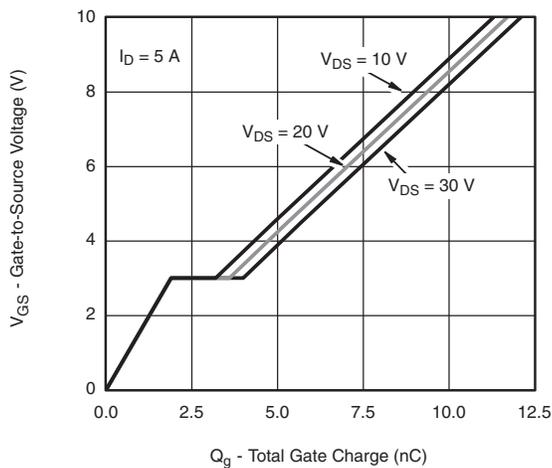
Transfer Characteristics



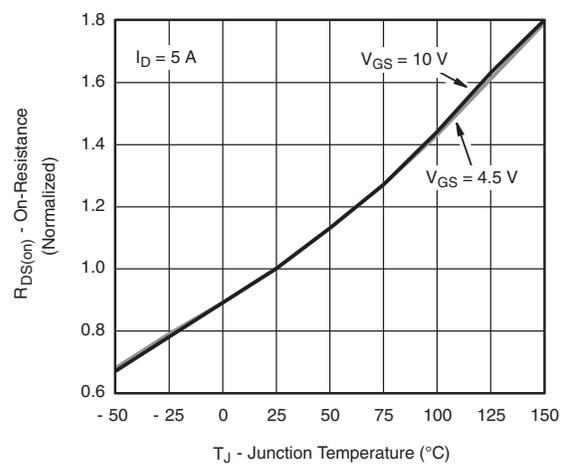
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



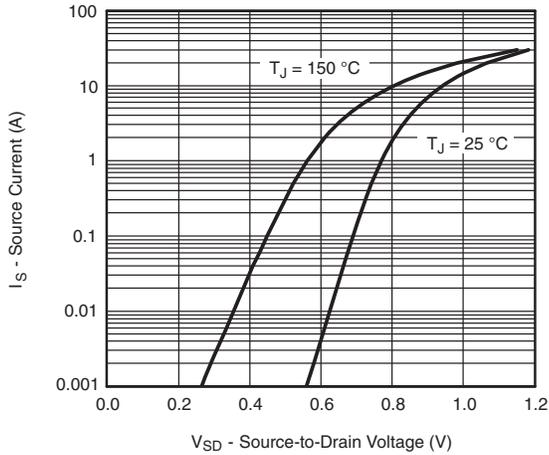
Gate Charge



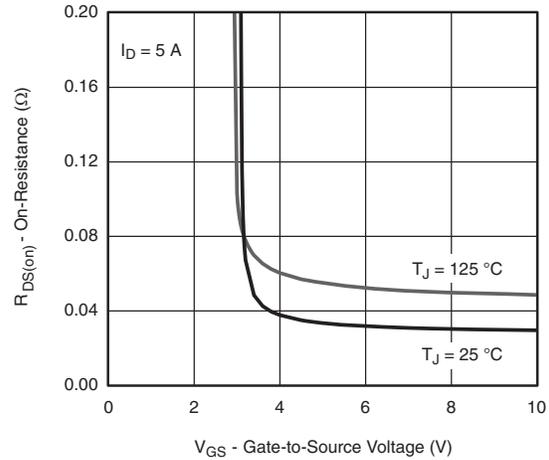
On-Resistance vs. Junction Temperature



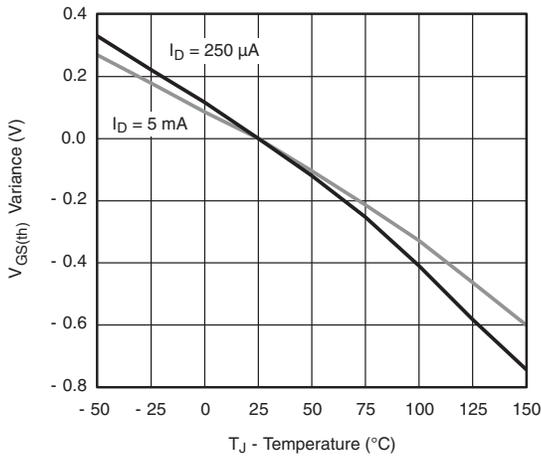
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



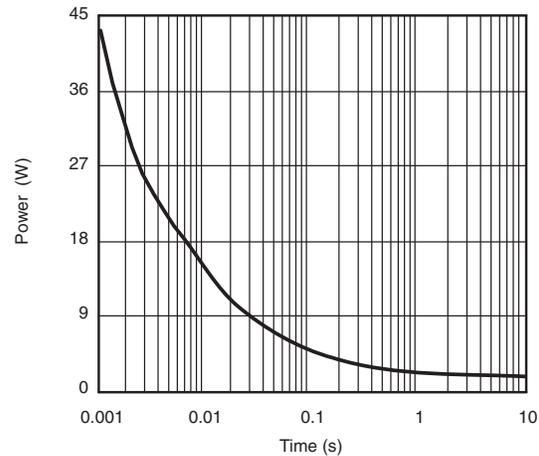
Source-Drain Diode Forward Voltage



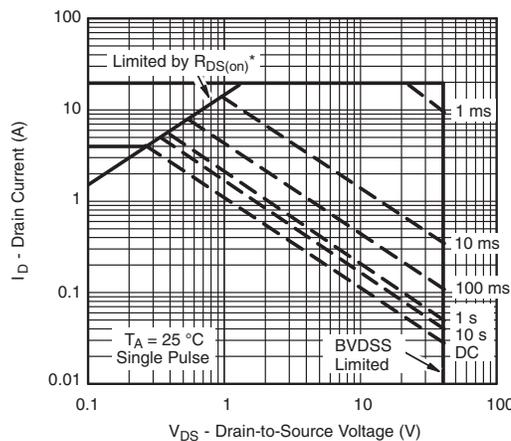
On-Resistance vs. Gate-to-Source Temperature



Threshold Voltage



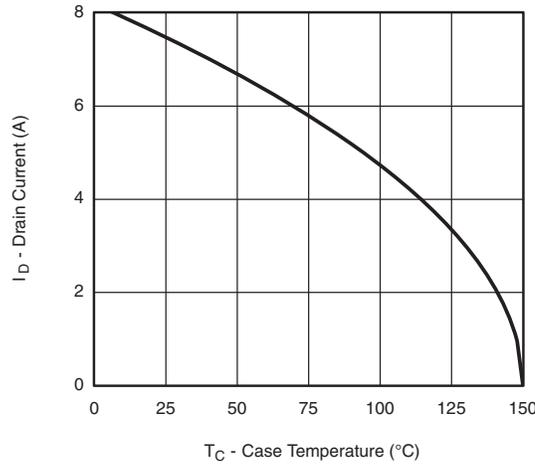
Single Pulse Power, Junction-to-Ambient



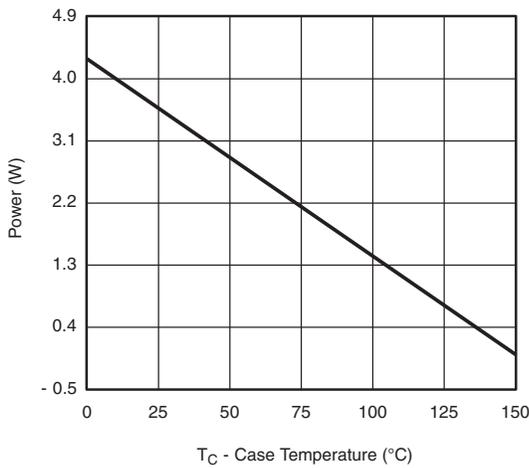
Safe Operating Area, Junction-to-Ambient



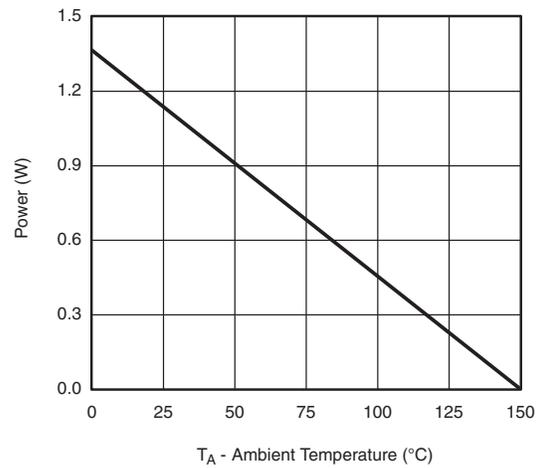
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating ^a



Power Derating, Junction-to-Foot



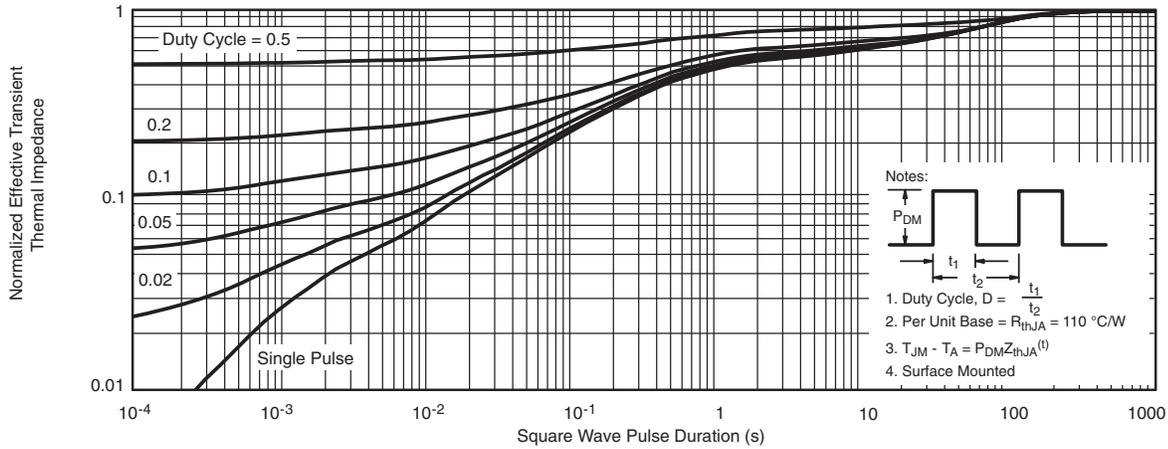
Power Derating, Junction-to-Ambient

Note

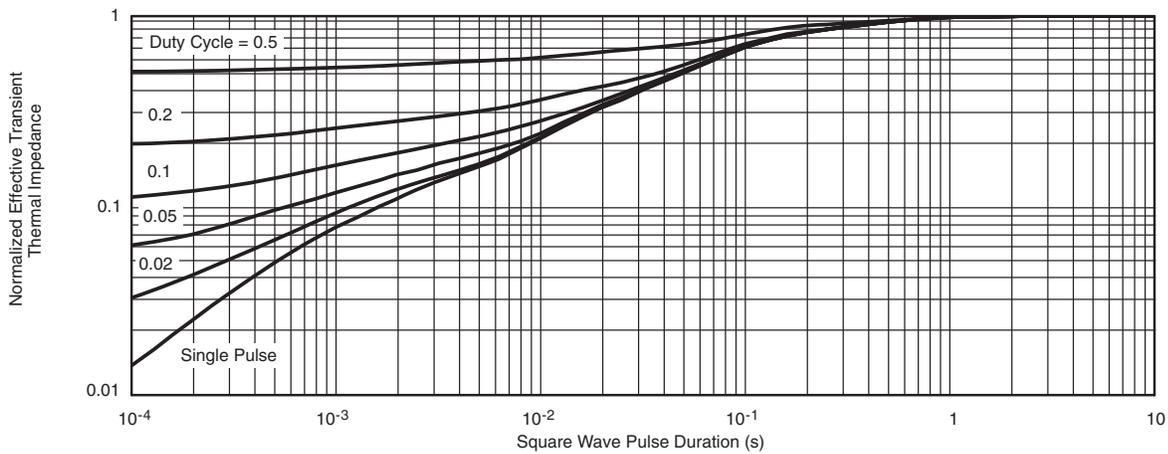
- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

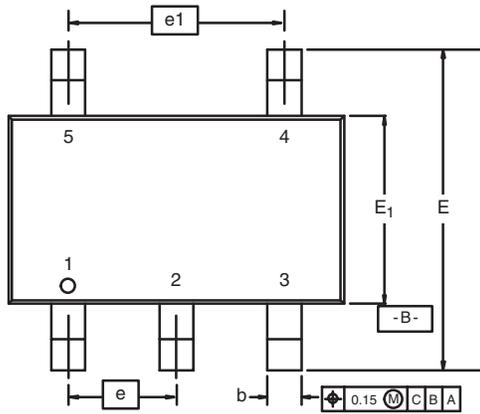


Normalized Thermal Transient Impedance, Junction-to-Foot

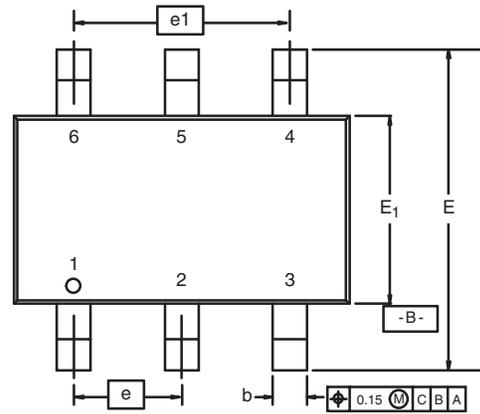
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68393.

TSOP: 5/6-LEAD

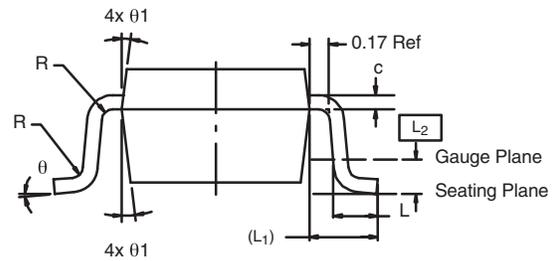
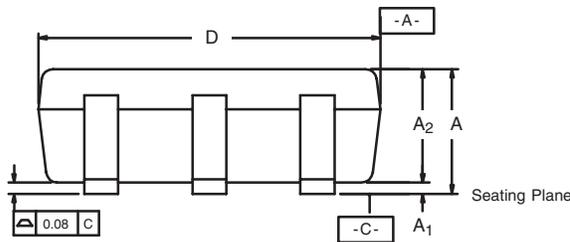
JEDEC Part Number: MO-193C



5-LEAD TSOP



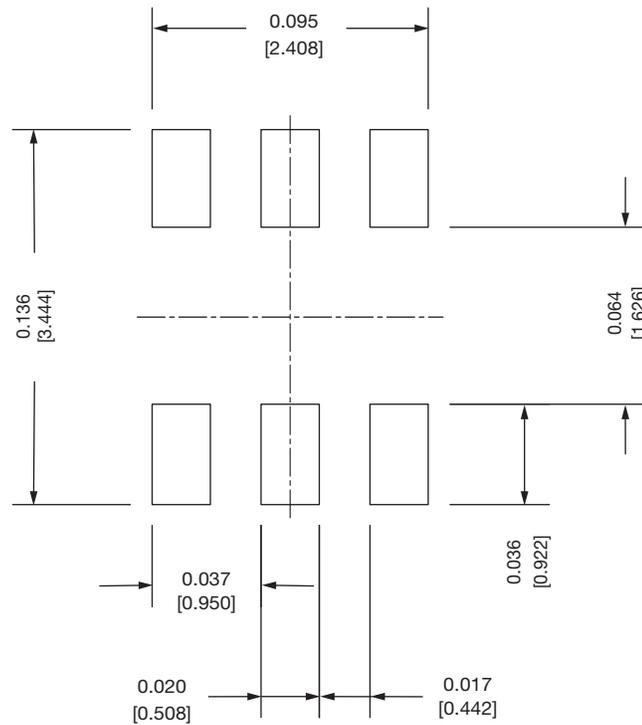
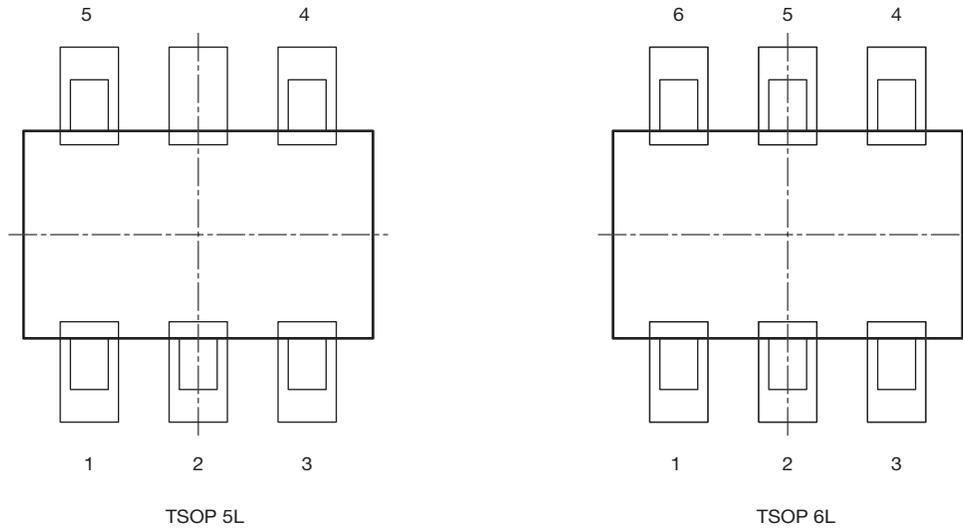
6-LEAD TSOP



Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.91	-	1.10	0.036	-	0.043
A₁	0.01	-	0.10	0.0004	-	0.004
A₂	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E₁	1.55	1.65	1.70	0.061	0.065	0.067
e	0.95 BSC			0.0374 BSC		
e₁	1.80	1.90	2.00	0.071	0.075	0.079
L	0.32	-	0.50	0.012	-	0.020
L₁	0.60 Ref			0.024 Ref		
L₂	0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ₁	7° Nom			7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06						
DWG: 5540						



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

- All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022
 DWG: 3010



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