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Vishay Siliconix

N-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.00200			
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 7.5 \text{ V}$	0.00250			
Q _g typ. (nC)	141			
I _D (A)	150 ^d			
Configuration	Single			

FEATURES

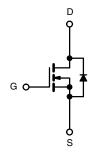
- TrenchFET® power MOSFET
- Maximum 175 °C junction temperature



- \bullet Very low Q_{gd} reduces power loss from passing through $V_{plateau}$
- 100 % R_a and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Power supply
 - Secondary synchronous rectification
- DC/DC converter
- Power tools
- · Motor drive switch
- DC/AC inverter
- · Battery management
- OR-ing / e-fuse



N-Channel MOSFET

ORDERING INFORMATION	
Package	TO-220
Lead (Pb)-free and halogen-free	SUP50010E-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	60	V	
Gate-source voltage		V _{GS}	± 20	v	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		150 ^d		
	T _C = 70 °C	I _D	150 ^d	^	
Pulsed drain current (t = 100 μs)		I _{DM}	500	A	
Avalanche current		I _{AS}	60		
Single avalanche energy ^a L = 0.1 mH		E _{AS}	180	mJ	
Maximum power dissipation ^a	T _C = 25 °C	D	375 b	W	
	T _C = 125 °C	P _D	125 ^b	vv	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient (PCB mount) ^c	R _{thJA}	40	°C/W	
Junction-to-case (drain)	R _{thJC}	0.4		

Notes

- a. Duty cycle ≤ 1 %
- b. See SOA curve for voltage derating
- c. When mounted on 1" square PCB (FR4 material)
- d. Package limited



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60	-	-	V
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	-	4	
Gate-body leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA
Zero gate voltage drain current		V _{DS} = 60 V, V _{GS} = 0 V	-	-	1	μΑ
	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	150	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 175 °C	-	-	5	mA
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	120	-	-	Α
Dualing and the second and a		V _{GS} = 10 V, I _D = 30 A	-	0.00166	0.00200	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 7.5 V, I _D = 20 A	-	0.00208	0.00250	Ω
Forward transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	-	120	-	S
Dynamic ^b						
Input capacitance	C _{iss}		-	10 895	-	pF
Output capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$	-	2420	-	
Reverse transfer capacitance	C _{rss}		-	85	-	
Total gate charge ^c	Qg		-	141	212	
Gate-source charge ^c	Q _{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	43.6	-	nC
Gate-drain charge ^c	Q_{gd}		-	19.1	-	
Output charge	Q _{oss}	V _{DS} = 50 V, V _{GS} = 0 V	-	143	215	
Gate resistance	R _g	f = 1 MHz	0.24	1.2	2.4	Ω
Turn-on delay time ^c	t _{d(on)}		-	28	56	
Rise time ^c	t _r	$V_{DD} = 30 \text{ V}, \text{ R}_L = 3 \Omega$ $I_D \cong 10 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$	-	12	24	
Turn-off delay time ^c	t _{d(off)}		-	50	100	- ns
Fall time ^c	t _f		-	13	26	
Drain-Source Body Diode Ratings	and Characte	ristics ^b (T _C = 25 °C)				
Pulsed current (t = 100 μs)	I _{SM}		-	-	250	Α
Forward voltage a	V _{SD}	I _F = 10 A, V _{GS} = 0 V	-	0.8	1.5	V
Reverse recovery time	t _{rr}	I _F = 34 A, di/dt = 100 A/μs	-	75	150	ns
Peak reverse recovery charge	I _{RM(REC)}		-	2.8	5.6	Α
Reverse recovery charge	Q _{rr}		-	0.12	0.24	μC
Reverse recovery fall time	ta		-	38	-	200
Reverse recovery rise time	t _b		-	37	-	ns

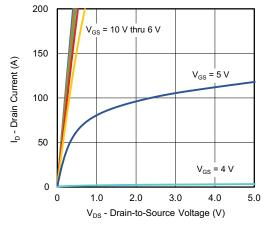
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

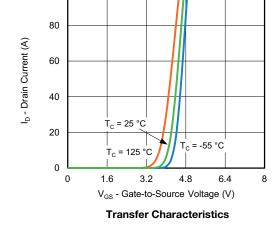
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



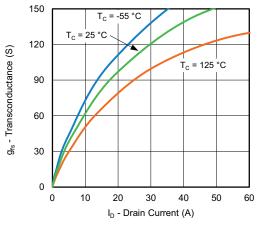
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



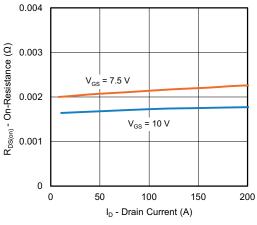
Output Characteristics



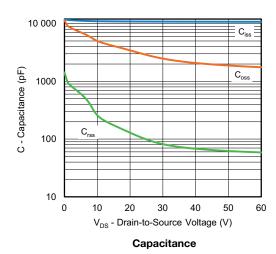
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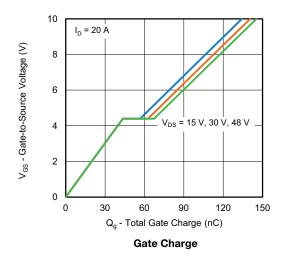


Transconductance



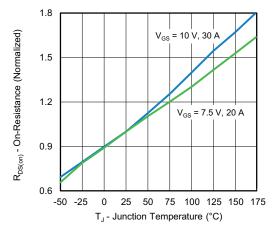
On-Resistance vs. Drain Current



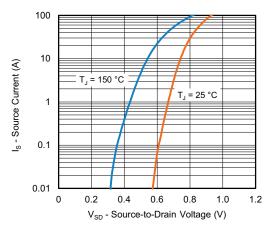




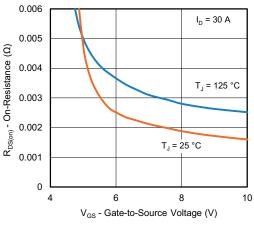
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



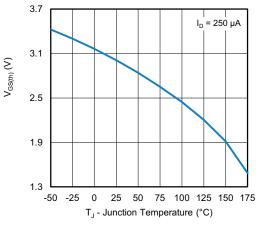
On-Resistance vs. Junction Temperature



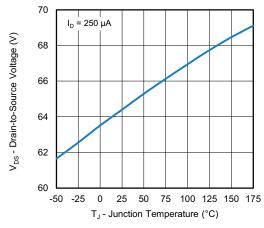
Source Drain Diode Forward Voltage



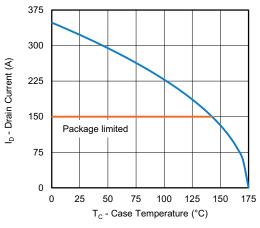
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



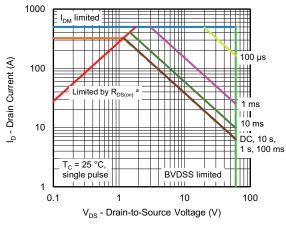
Drain Source Breakdown vs. Junction Temperature

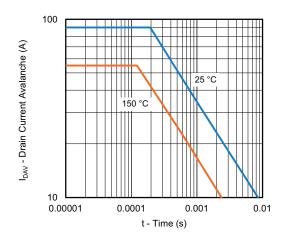


Current De-rating



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



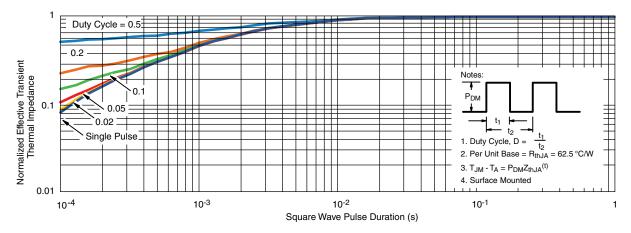


Single Pulse Avalanche Current Capability vs. Time

Safe Operating Area

Note

a. V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

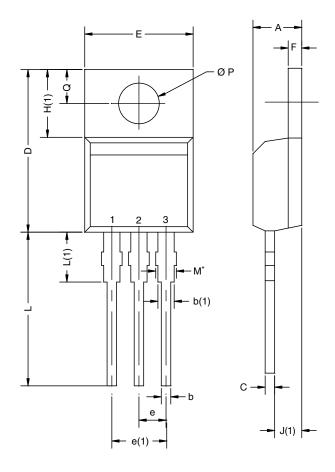
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual pplication parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?79578.





TO-220AB



	D2

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

Note

 $^{^{\}star}$ M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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