



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	Ι _D Τ _A = +25°C
20V	0.17Ω @ V _{GS} = 4.5V	1.35A
	0.23Ω @ V _{GS} = 2.5V	1.15A
	0.25Ω @ V _{GS} = 1.8V	1.10A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

Features

- Low On-Resistance
- Very Low Gate Threshold Voltage V_{GS(TH)}, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

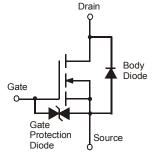
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 4
- Weight: 0.001 grams (approximate)

X1-DFN1006-3





Top View Internal Schematic



Equivalent Circuit

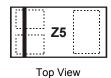
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2250UFB-7B	X1-DFN1006-3	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



Z5 = Product Type Marking Code Bar Denotes Gate and Source Side



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ΙD	1.35 1.03	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	6	Α
Maximum Body Diode continuous Current			Is	1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

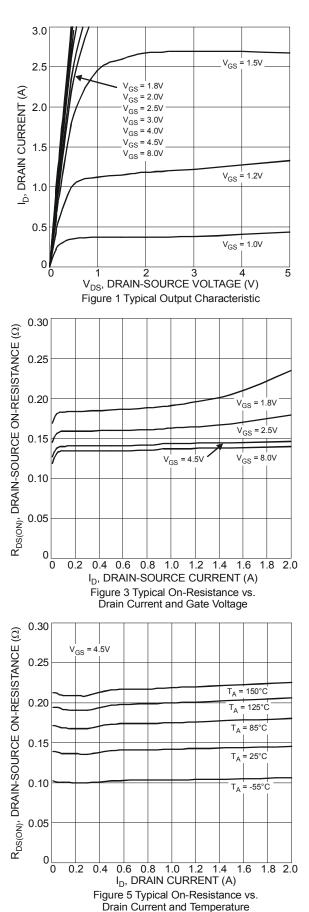
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	ם	0.5	W
Total Power Dissipation (Note 5)	T _A = +70°C	P_{D}	0.3	
Thermal Resistance, Junction to Ambient (Note 5) Steady state		$R_{ heta JA}$	278	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

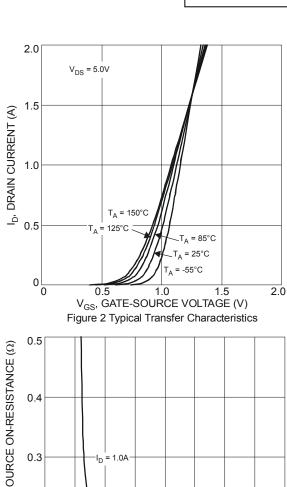
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

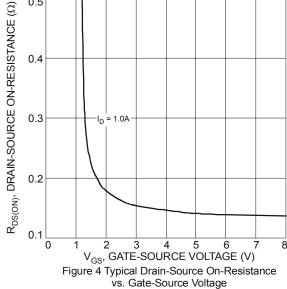
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±1	μΑ	$V_{GS} = \pm 6V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(th)}$	0.35	_	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
			_	170		$V_{GS} = 4.5V$, $I_{D} = 1A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	_	230	mΩ	$V_{GS} = 2.5V, I_D = 1A$	
			_	250		$V_{GS} = 1.8V, I_D = 1A$	
Forward Transfer Admittance	Y _{fs}	_	1.4	_	S	$V_{DS} = 10V, I_{D} = 1A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_S = 150mA$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}		94	_	pF	\\ -46\\ \\ -0\\	
Output Capacitance	Coss		12	_	pF	V _{DS} =16V, V _{GS} = 0V, -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		10	_	pF	1 - 1.0WH12	
Gate resistance	R_g	I	87.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_g		1.4	_	nC		
Total Gate Charge (V _{GS} = 10V)	Q_g	_	3.1	_	nC)/ - 10\/ - 250m A	
Gate-Source Charge	Qgs	_	0.13	_	nC	V _{DS} = 10V, I _D = 250mA	
Gate-Drain Charge	Q_{qd}		0.14	_	nC		
Turn-On Delay Time	t _{D(on)}	_	4.3	_	ns	V _{DD} = 10V, V _{GS} = 4.5V,	
Turn-On Rise Time	ì,		6.1	_	ns		
Turn-Off Delay Time	t _{D(off)}		59.4	_	ns	$R_L = 47\Omega, R_G = 10\Omega,$	
Turn-Off Fall Time	t _f		25.4	_	ns	I _D = 200mA	

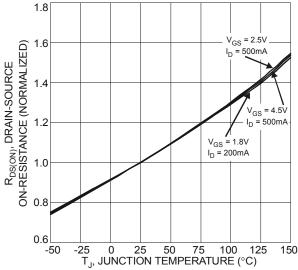
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.6. Short duration pulse test used to minimize self-heating effect.7. Guaranteed by design. Not subject to product testing.



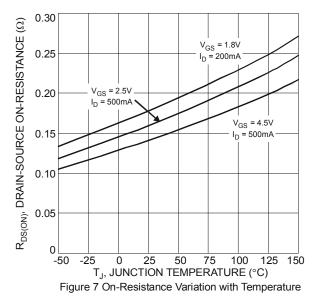


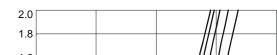


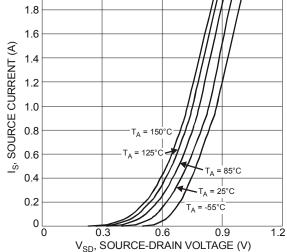


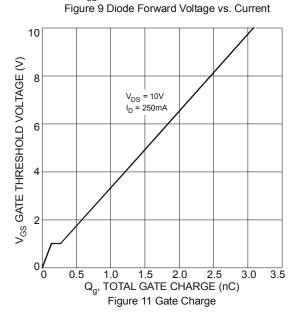












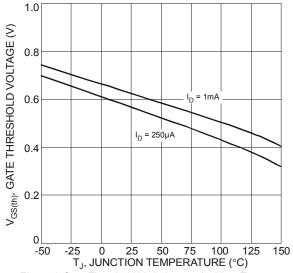
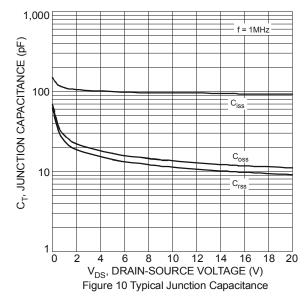


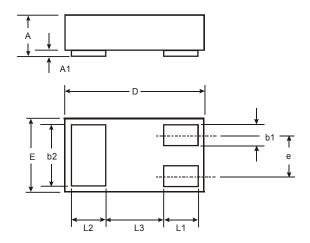
Figure 8 Gate Threshold Variation vs. Ambient Temperature





Package Outline Dimensions

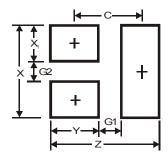
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



X1-DFN1006-3						
Dim	Min	Max	Тур			
Α	0.47	0.53	0.50			
A1	0	0.05	0.03			
b1	0.10	0.20	0.15			
b2	0.45	0.55	0.50			
D	0.95	1.075	1.00			
Е	0.55	0.675	0.60			
е		_	0.35			
L1	0.20	0.30	0.25			
L2	0.20	0.30	0.25			
L3		_	0.40			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7



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