COMPLIANT



Vishay Semiconductors

Schottky Rectifier, 3.0 A



PRODUCT SUMMARY				
Package	SMC			
I _{F(AV)}	3 A			
V_{R}	15 V			
V _F at I _F	0.3 V			
I _{RM}	50 mA at 100 °C			
T _J max.	125 °C			
Diode variation	Single die			
E _{AS}	1.5 mJ			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-30BQ015PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform		А			
V _{RRM}		15	V			
I _{FSM}	t _p = 5 μs sine	650	А			
V _F	1.0 Apk, T _J = 75 °C	0.30	V			
TJ	Range	- 55 to 125	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-30BQ015PbF	UNITS
Maximum DC reverse voltage	V _R	15	V
Maximum working peak reverse voltage	V _{RWM}	25	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum avarage forward current		50 % duty cycle at T _L = 83 °C, rectangular waveform		3.0	
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 78 °C, rectangular waveform		4.0	
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	650	Α
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.5 A, L = 12 mH		1.5	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		0.5	Α

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Marine Constitution of the	V (1)	3 A	- T _J = 25 °C	0.35	V
		6 A		0.40	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T _J = 75 °C	0.30	
		6 A		0.35	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	4	mA
iviaximum reverse leakage current	IRM (")	T _J = 100 °C		50	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1120	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 0		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		- 55 to 125	۰,0
Maximum storage temperature range	T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DO acception	12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	
Approximate weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (similar to DO-214AB)	V3	BC

Notes

 $[\]frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB



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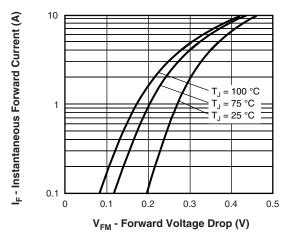


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

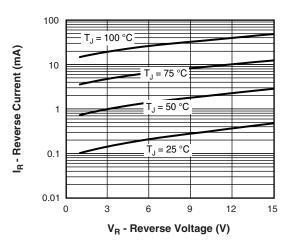


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

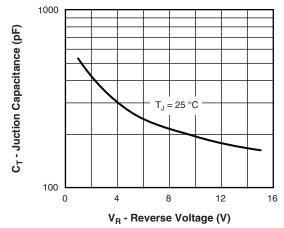


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

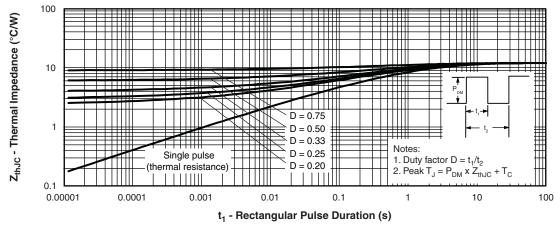


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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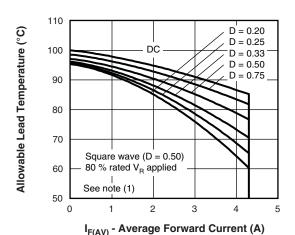


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

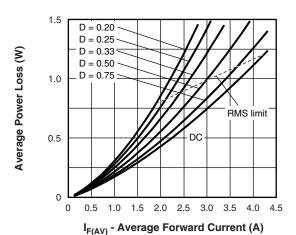


Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

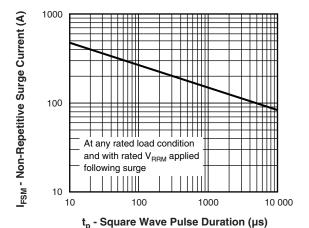


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

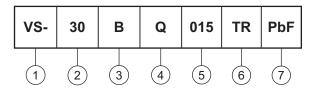


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ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

2 - Current rating

B = Single lead diode

4 - Q = Schottky "Q" series

- Voltage rating (015 = 15 V)

None = Box (1000 pieces)

• TR = Tape and reel (3000 pieces)

7 - PbF = Lead (Pb)-free

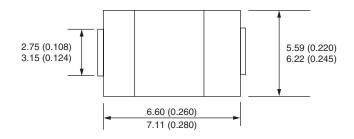
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95023</u>				
Part marking information		www.vishay.com/doc?95029		
Deckaging information	Tape and reel	www.vishay.com/doc?95034		
Packaging information -	Bulk	www.vishay.com/doc?95397		
SPICE model		www.vishay.com/doc?95439		

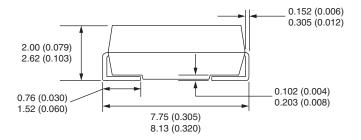


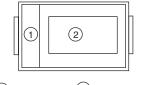
Vishay High Power Products

SMC

DIMENSIONS in millimeters (inches)

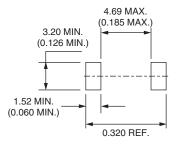






1 Polarity

2 Part number



Soldering pad



Legal Disclaimer Notice

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