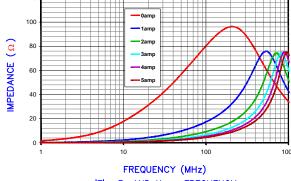


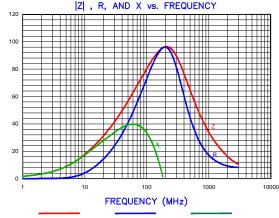
## PHYSICAL DIMENSIONS:

A 2.00 [.079] ± 0.20 [.008] B 1.25 [.049] ± 0.20 [.008] C 0.90 [.035] ± 0.20 [.008]

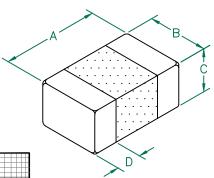
D 0.51 [.020] ± 0.25 [.010]

# Z vs FREQUENCY IMPEDANCE UNDER DC BIAS





AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3796

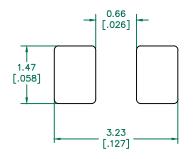


ELEC.	ELECTRICAL CHARACTERISTICS:							
Z @ 100M ( Ω )	···· · <del>-</del>	DCR ( $\Omega$ )	Rated Current					
Nominal	80							
Minimum	60							
Maximum	100	0.010	5000 mA					

### NOTES: UNLESS OTHERWISE SPECIFIED

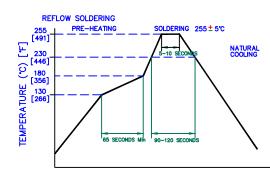
- TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REE, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. CONTINUOUS CURRENT RATING OF 5000 mA.
- 4. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 5. OPERATING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

#### LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

#### RECOMMENDED SOLDERING CONDITIONS



D] to this dimension.)												
DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird	Т							
				Tech, and is loaned in confidence subject to return upon request a	al I			T,				
_				with the understanding that no	٦I	Laird						
_				copies shall be made without the written consent of Laird Tech. All	י ו			<b>4</b>				
				rights to design or invention are	-		_					
E	OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13		reserved. PROJECT/PART NUMBER:	$\perp$							
_		/ /			REV	PART	TYPE:	DRAWN BY:				
┖		03/03/10		HI0805R800R-10	ΙE	CC	-FIRE	І тив				
C	UPDATE COMPANY LOGO	06/15/09	JRK		1 -			"""				
В	UPDATE COMPANY LOGO	01/17/08	JRK	DATE: 04/02/04 St	ALE:	NTS	SHEET:					
Α	ORIGINAL DRAFT	04/02/04	TMB	C4D 4	OL #		1	of 1				
EV	DESCRIPTION	DATE	INT	HI0805R800R-10-E		-	Ι '	J. 1				